

Review

Triple-Mesosopic Carbon Perovskite Solar Cells: Materials, Processing and Applications

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Abstract: Perovskite solar cells (PSCs) have already achieved comparable performance to industrially established silicon technologies. However, high performance and stability must be also be achieved at large area and low cost to be truly commercially viable. The fully printable triple-mesosopic carbon perovskite solar cell (mCPSC) has demonstrated unprecedented stability and can be produced at low capital cost with inexpensive materials. These devices are inherently scalable, and large-area modules have already been fabricated using low-cost screen printing. As a uniquely stable, scalable and low-cost architecture, mCPSC research has advanced significantly in recent years. This review provides a detailed overview of advancements in the materials and processing of each individual stack layer as well as in-depth coverage of work on perovskite formulations, with the view of highlighting potential areas for future research. Long term stability studies will also be discussed, to emphasise the impressive achievements of mCPSCs for both indoor and outdoor applications.

Keywords: perovskite; carbon; screen-printing; stability



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1. Introduction

Perovskite solar cells (PSCs) represent an exciting new thin-film technology [1–6]. There are potentially a vast number of creative applications for perovskites, and these devices are perhaps uniquely versatile in that PSCs have achieved high efficiencies in a variety of different architectures [7–10]. PSC architectures have use of mesoporous layers and planar structures, and swapped or completely eliminated the charge transport layers [11]. This is possible because of the intrinsic perovskite ambipolarity [12,13].

The triple-mesosopic carbon PSC (mCPSC) is unusual compared to other thin-film PSC architectures. First introduced in 2013 by Han and co-workers [14], devices consist of a triple-mesosopic stack consisting of a mesoporous electron transport layer (ETL) (TiO_2), mesoporous insulating layer (usually ZrO_2) and a porous, conductive carbon top contact. No hole transport layer (HTL) is required in this architecture. Layers are screen-printed onto a conductive glass substrate coated with compact TiO_2 and annealed at high temperature to remove organic binders before subsequent infiltration of the perovskite ink. This means that the perovskite is hosted in all the three layers of the micrometre-thick stack, as opposed to being a sandwich structure between very thin electrodes (Figure 1).

Under light, photogenerated electrons are injected into the TiO_2 , which is in close contact with the transparent electrode. Holes are transferred through the perovskite in the insulating layer and must travel several micrometers before injection into the carbon (Figure 2). This architecture, therefore, reveals that photogenerated electron-hole pairs in perovskites have remarkably long lifetimes.

As the entire stack is fully printable, mCPSCs have high potential for scale-up. The rigid glass substrates, used as they can withstand high temperature annealing, are most suited to sheet-to-sheet (S2S) batch production. The screen-printing processes commonly used to print the stack are also particularly well suited to S2S manufacture, although a roll-to-roll (R2R) production is feasible via rotary screen-printing. Layers can be also

deposited via slot-die coating [17], which would be ideal for a R2R process on flexible substrates. The manufacture of mCPSC on flexible substrates is still limited by the high curing temperature ($>400\text{ }^{\circ}\text{C}$), but it represents a fascinating challenge for future research.

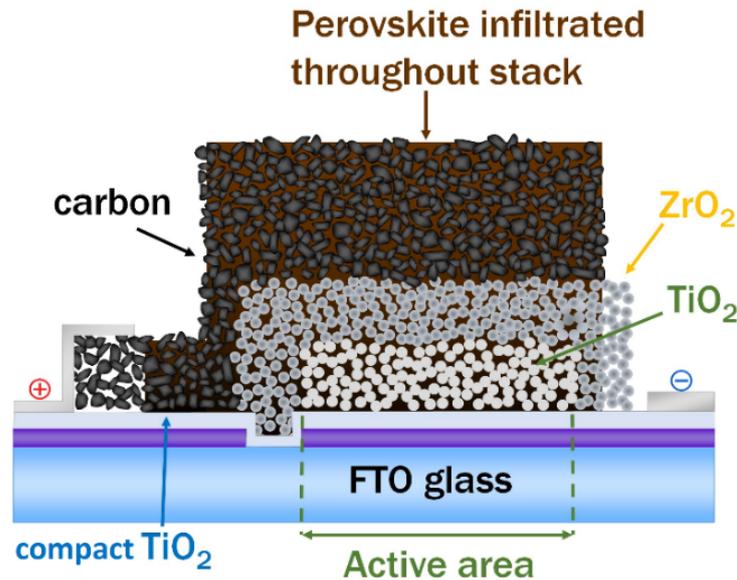


Figure 1. Schematic of a typical triple-mesoscopic carbon perovskite solar cell (mCPSC). The TiO_2 , ZrO_2 and carbon of the triple-mesoscopic stack are deposited sequentially before infiltration of the perovskite precursor solution. Adopted from reference [15] with permission from The Royal Society of Chemistry.

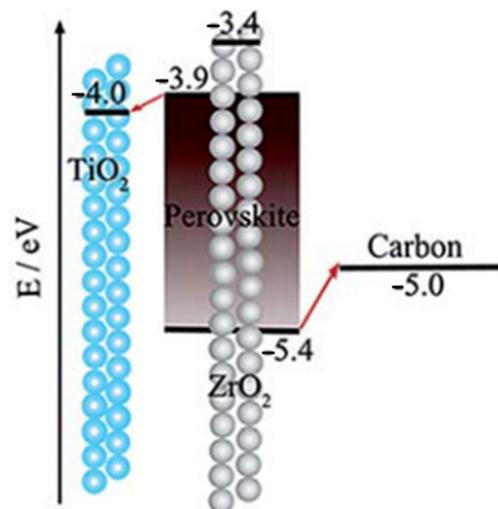


Figure 2. Energy level diagram of a typical mCPSC. Adopted from reference [16] with permission from The Royal Society of Chemistry.

The thermal treatment can be optimised via near-infrared (NIR) fast annealing for rapid production and low energy consumption. This fast annealing can decrease the cost of production [15]. Module interconnects can be accurately created via a low-cost rapid material removal approach known as the scribing method. With the use of screen-printing, scribing method and fast annealing process such as Near-Infrared (NIR) a possible production line emerges, schematically shown in Figure 3.

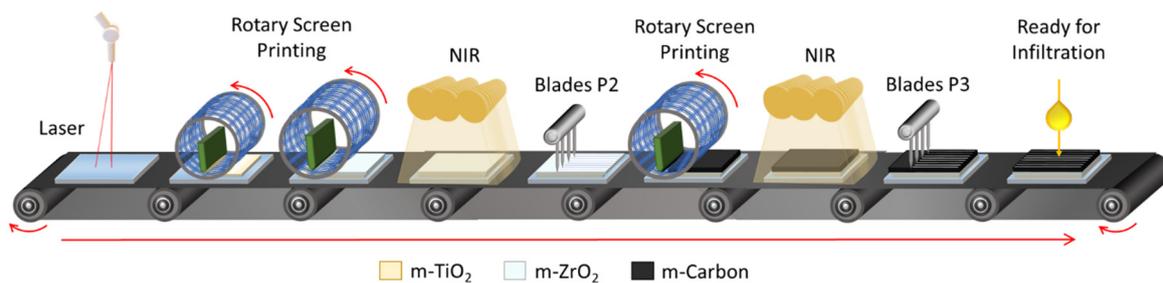


Figure 3. Schematic representation of a possible mCPSC production line using screen-printing, scribing method and NIR fast annealing.

Other PSCs with non-porous carbon top electrodes atop the perovskite layer in conventional sandwich structure have also been proposed [18]. In these devices, the carbon directly replaces the metal top contact of a typical PSC, allowing the preparation of low temperature devices. This is a potential advantage over mCPSCs, which require high temperature annealing to remove organic paste binders and form the porous stack [19]. Consequently, mCPSCs have yet to be deposited on flexible substrates such as PET, while this has been already achieved with the other carbon architectures [20].

However, these devices often exhibit inferior stability (Section 4). Furthermore, chemical interactions between the perovskite crystals and some carbon inks can cause degradation or poor performance [15,21]. This is not a problem in mCPSCs, as binder is burned away before perovskite infiltration, allowing for more flexibility when designing ink formulations.

Another advantage of the mCPSC is the potential for pre-fabricating a large number of stacks on glass and infiltrating the perovskite precursor solution as needed to produce devices on demand. This characteristic allows for the preparation of preassembled products and would enable fast delivery in an industrial setting. Finally, the mCPSC is low-capital cost as all the required equipment to deposit the layers on a glass/FTO/compact TiO₂ substrate is inexpensive (FTO, F:SnO₂). The low-capital cost is a key aspect of this technology—although expensive equipment, such as high-tech screen-printers or NIR heating elements can be used for high volume production, extremely low-cost equipment can also be applied, especially when mechanical scribes or manual screen printers are adopted. In other words, the lab-to-fabrication process is cheaper and easier than for other PSC architectures, and the small initial investment has the potential to pay back over a shorter period for both large companies and small enterprises.

Interest in mCPSCs has therefore increased rapidly due to the high potential for achieving short-term market penetration [22,23]. In particular:

- Screen-printing is an established industrial method for large volume production and up-scaling mCPSC manufacture is therefore straightforward. As mentioned above, equipment is also inexpensive, decreasing the financial risks associated with production line setup.
- Low material costs—unlike in many conventional PSCs, expensive noble metals and organics that require costly purification processes (like spiro-OMeTAD) are not required [24];
- The reported stability data (over one-year) under continuous illumination are promising in terms of long-term applications, far outstripping other PSC architectures [25–28].
- Module performance is not particularly affected by the size of the device, as often happens for other PSC architecture [29].

Modules, i.e., devices based on single cells linked by an interconnect, can be prepared via a pure registration approach [30,31] or scribing [32,33]. Crucial in large area devices, interconnects help overcome the issue of the resistance introduced by large substrates. Interconnects can be used to create parallel or series-connected modules to allow charge collection close as possible to the generation point.

The registration method exploits the ability of printing technologies to pattern each layer. When these layers are accurately registered in the exact designed position, it is possible

to create interconnects during the initial print. Printing must be incredibly accurate, as any misalignment can cause shorts between layers. Print precision relies on the technology of the screen-printer. The accuracy of a given printer may be enhanced using camera systems and software, however this can increase cost and/or slow down the production rate.

Alternatively, in the scribing method layers are deposited as solid areas and then scribed using a blade or a laser, to disconnect specific layers or open an aperture for interconnects. Three scribes called P1, on the bottom electrode, P2, through the mesoporous TiO_2 and ZrO_2 layers, and P3, on the top electrode, create the necessary interconnection in a series-connected module (Figure 4). A material removal approach that requires less expensive equipment, the scribing method can be much faster than the registration method and suits either a S2S or a R2R production. This method is widely used in thin film solar modules with high active/substrate area ratio (geometric fill factor, g-FF).

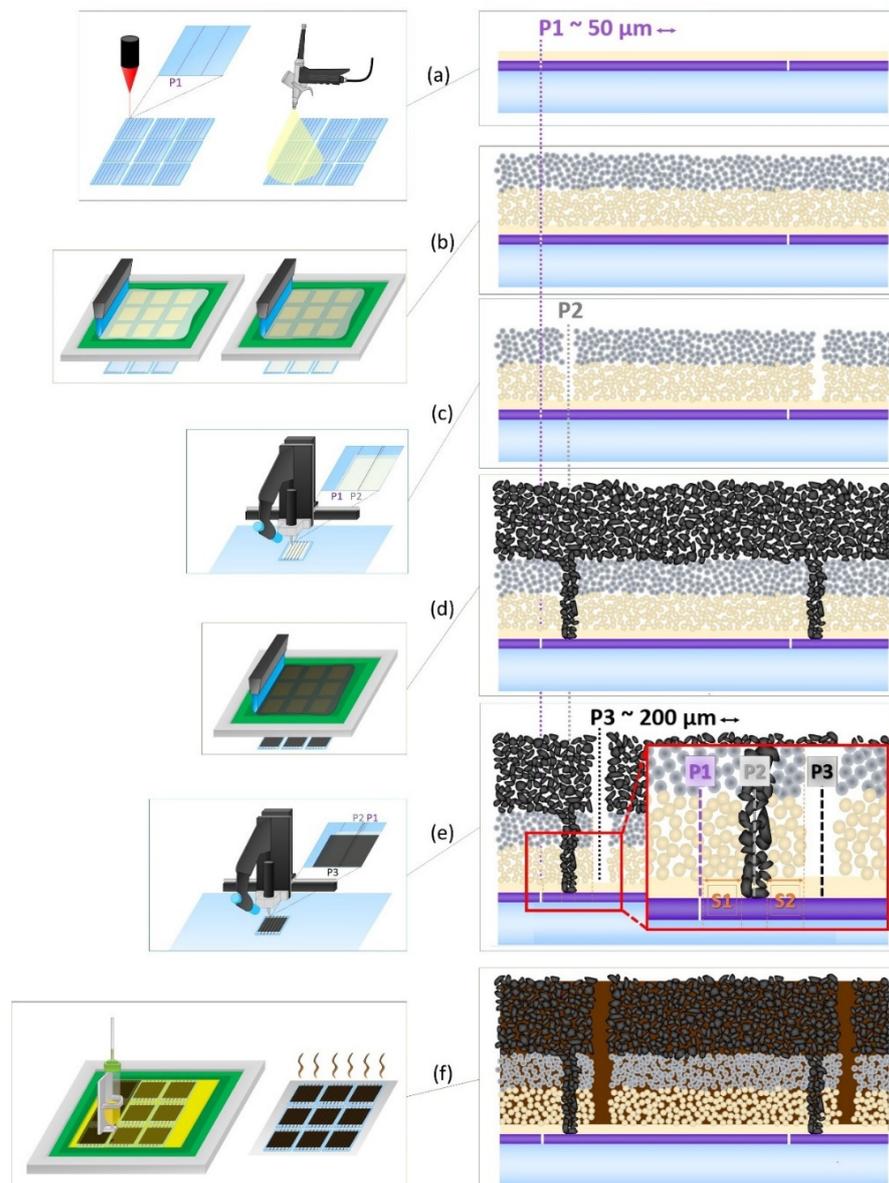


Figure 4. The mCPSC module based on the scribing method. (a) P1 on a conductive substrate. (b) Deposition of mesoporous TiO_2 and ZrO_2 layers *via* screen-printing. (c) P2 *via* mechanical scribing to open an aperture for the interconnects. (d) Deposition of the top carbon electrode. (e) P3 to insulate the cells in series. In the inset, a magnification of the interconnection area. (f) Perovskite infiltration. Reproduced from reference [32].

Perhaps the least inherently scalable step in mCPSC manufacture is the infiltration of the perovskite ink. This is usually done via drop casting at lab scale. The method is quick and simple when making small single cells, but it is prone to human error and becomes time-consuming and complex at larger scale. Alternative methods suitable for larger devices and high throughput production have been explored, including inkjet printing [34,35], robotic dispensers and mesh (RbM) [31,32,36] and slot-die coating [33,37].

The mCPSCs have also many interesting avenues for fundamental research. For example, exceptional stability has been observed with the addition of 5-aminovaleric acid ($\text{NH}_3(\text{CH}_2)_4\text{COOH}^+$, AVA) into methylammonium lead iodide perovskite ($\text{CH}_3\text{NH}_3\text{PbI}_3$, MAPbI₃) [25,28]. This resulted in astonishing stability results, with devices passing IEC61215:2016 standards and 9000 h under maximal power point tracking without degradation [28]. The importance of the perovskite formulation and the work done in recent years in advancing performance and stability will be discussed in detail in Section 3. Many aspects related to the high stability of mCPSCs, such as the long charge lifetime and the surprising benefits of high humidity device exposure [38] require clarification.

2. Modifications to the Triple Mesoscopic Stack

The chemical, physical and morphological properties of the inorganic mesoporous stack play an important role in determining device performance. The most influential variations in the inorganic stack trialled thus far are reported in this section.

2.1. TiO_2 Layer

TiO_2 is present in the mCPSC architecture as both a compact and mesoporous layer, and respectively act as a hole barrier and electron transporting layer (ETL). Despite much research on alternative ETLs for other devices [39–41], TiO_2 is so far the only n-type material tested in mCPSCs, leaving this area open to further investigation. Both band alignment and thermal stability are important characteristics mCPSC ETLs, as layers are subject to multiple high temperature annealing steps to remove binders and create the porous scaffold.

Compact layer thickness heavily impacts performance, with a particularly large effect on device hysteresis [42]. However, the effect of changing ETL morphology, chemical composition or doping has yet to be explored in depth.

The mesoporous layer tends to be between 0.4 μm and 1 μm thick [27,30,43,44], and provides a very large interface with the perovskite to maximise charge extraction [27,30,43,45]. In 2017 Priyadarshi et al. reported a simplified architecture without this layer, showing working devices with reduced power conversion efficiencies (PCE). The observed performance reduction can be minimised by using thicker ZrO_2 to compensate for the loss of the mesoporous TiO_2 layer [46]. Specifically, when ZrO_2 was increased from 1.4 to 2.1 μm , devices achieved nearly the same performance as controls incorporating a standard 0.5 μm TiO_2 layer (11.9% PCE standard architecture, 7.8% PCE without TiO_2 , and 9.7% PCE with thicker ZrO_2).

In terms of nanoparticle size, an optimal dimension of 20–30 nm in diameter allows large pore sizes for effective infiltration [16]. Mixed size nanoparticle systems (25 nm and 100 nm) have been shown to further increase layer porosity and improve perovskite infiltration [47]. It is also possible to enhance device performance through incorporating additional materials into the mesoporous TiO_2 . For example, Xu and co-workers added EuW10, a luminescence down-conversion material, to the TiO_2 paste before printing [48], resulting in a PCE improvement from 11.42% to 14.36% due to increases in short circuit current (J_{sc}) and fill factor (FF).

Very recently, Sheng et al. presented a simple and effective method to modify the ETL and improve the device photovoltage. Annealed stacks were treated with 2-phenyl-5-benzimidazole sulfonate-Na before infiltration, to shift the TiO_2 conduction band from -4.22 to -4.11 eV. As a result, the electron transfer from the perovskite absorber to the ETL was enhanced and recombination at the ETL/perovskite interface suppressed. Consequently, the open circuit voltage (V_{oc}) and FF of the devices were increased without sacrificing the J_{sc} , with the champion device showing an overall PCE of 16.51% [49].

2.2. Insulating Layer

The insulating or spacer layer is crucial to the mCPSC as it separates the ETL from the top electrode. This prevents the formation of a TiO_2 /carbon interface, which would act as a Schottky diode and drive recombination. This layer is not present in other architectures, where either an HTL, ETL or perovskite capping layer provides the necessary separation [13,18,50,51]. In principle, the spacer layer can consist of any insulating material thermally stable enough to survive the high temperature treatments used to remove paste binders. The most widely adopted material is ZrO_2 , although Al_2O_3 and SiO_2 have also been reported.

Layer thickness is important—the spacer should be thick enough to avoid any pinholes and properly separate the carbon from the TiO_2 , while being thin enough not to hinder charge transport or infiltration. According to early reports, the optimal spacer thickness for TiO_2 / ZrO_2 /carbon stacks infiltrated with MAPbI_3 is between 1 and $1.4\ \mu\text{m}$ [52]. Another optimisation study on AVA- MAPbI_3 devices by Hu et al. on a $0\text{--}4\ \mu\text{m}$ ZrO_2 range with perovskite found that a $2\ \mu\text{m}$ ZrO_2 layer exhibited optimal performance [30].

However, efficient AVA- MAPbI_3 devices of over 15% PCE can be achieved with $3\ \mu\text{m}$ spacer layer, as reported by Han and co-workers [53,54]. It appears that as long as the stack is well-infiltrated, devices with 2 or $3\ \mu\text{m}$ thick ZrO_2 layers show better performance than those with a $1.2\ \mu\text{m}$ [54]. Optimal ZrO_2 thickness for TiO_2 / ZrO_2 /carbon-AVA- MAPbI_3 mCPSC was systematically investigated by Liu et al. [55]. With an optimised ZrO_2 nanoparticle size of $20\ \text{nm}$, nearly 14% PCE was achieved with a $4.84\ \mu\text{m}$ spacer layer. In other words, although high quality infiltration becomes more challenging as the thickness of the spacer layer increases, when good infiltration is achieved, better performance is observed with a thicker insulating layer (Figure 5).

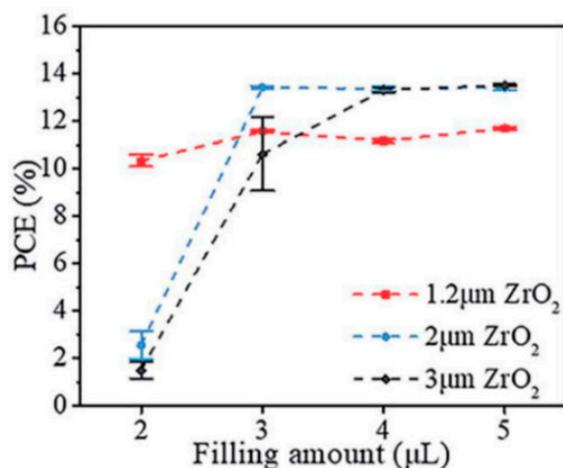


Figure 5. Infiltration volume effect on device PCE for stacks with 1.2, 2 and $3\ \mu\text{m}$ ZrO_2 spacer layer. The effect on the power conversion efficiency (PCE) is negligible when $1.2\ \mu\text{m}$ spacer layer is used but becomes very striking for thicker layers (2 and $3\ \mu\text{m}$). When the stack with a $3\ \mu\text{m}$ thick ZrO_2 layer is fully infiltrated, the final PCE is higher than that of the device with a $1.2\ \mu\text{m}$ ZrO_2 layer. Adopted from reference [54] with permission from The Royal Society of Chemistry.

The effect of increasing spacer layer thickness is incredibly unexpected and reveals several interesting perovskite properties. As most photogenerated charge is generated in the first few hundred nanometres of the stack [56], holes must travel extreme distances through the perovskite before collection at the carbon electrode. The path through the insulator layer is even longer than the layer is thick, as charges are limited to travelling between perovskite crystals in the mesoporous framework. Holes must therefore travel several μm simply to cross the insulating layer. As these distances are far larger than typical perovskite diffusion lengths it would be logical to assume that devices would exhibit low PCEs due to high levels of recombination. However, this is not the case: despite the long distance, enough charge can be extracted and collected to achieve over 13% PCE, and the

best results are often observed with thicker spacers. The reason for such performance enhancements is still unclear.

The effect of spacer layer thickness on PCE has been most thoroughly investigated for the $\text{TiO}_2/\text{ZrO}_2/\text{carbon-AVA-MAPbI}_3$ devices discussed above. However, it should be noted that optimal spacer thickness can vary when alternative perovskite formulations are used. For example, $\text{Cs}_{0.1}\text{Rb}_{0.05}\text{FA}_{0.85}\text{PbI}_3$ perovskite formulations achieved an outstanding 16% PCE with a ZrO_2 layer of only 750 nm [57]. Alternatively, a 17% PCE device using $\text{Cs}_{0.05}(\text{FA}_{0.4}\text{MA}_{0.6})_{0.95}\text{PbI}_{2.8}\text{Br}_{0.2}$ perovskite was reported in 2017 with optimised layer thicknesses of 460 nm, 450 nm, 800 nm and 10 μm for $\text{TiO}_2/\text{Al}_2\text{O}_3/\text{NiO}$ and carbon layers respectively [58]. Conversely, a study by Di Carlo and co-workers in 2019 found that a 1.2 μm thick Al_2O_3 was optimum for MAPbI_3 devices [59], with optimised devices of around 9.13% PCE compared to 8.28% for 0.8 μm and 6.29% for 1.8 μm .

Thinner, sputtered Al_2O_3 spacer layers in the order of hundreds of nm for AVA-MAPbI_3 devices have also recently been reported, proving that that mCPSCs can work without very thick spacer layers as long as the carbon layer is properly insulated [60]. Here, Al_2O_3 at thicknesses of 10, 20, 40 or 100 nm was sputtered onto screen-printed mesoporous TiO_2 (Figure 6). Optimal results were observed at 40 nm, with cells exhibiting very high V_{oc} (around 1 V) and comparable overall performance to standard screen-printed ZrO_2 devices. Devices with still-thin 100 nm spacer layer performed poorly, probably because a sputtered layer can impede infiltration.

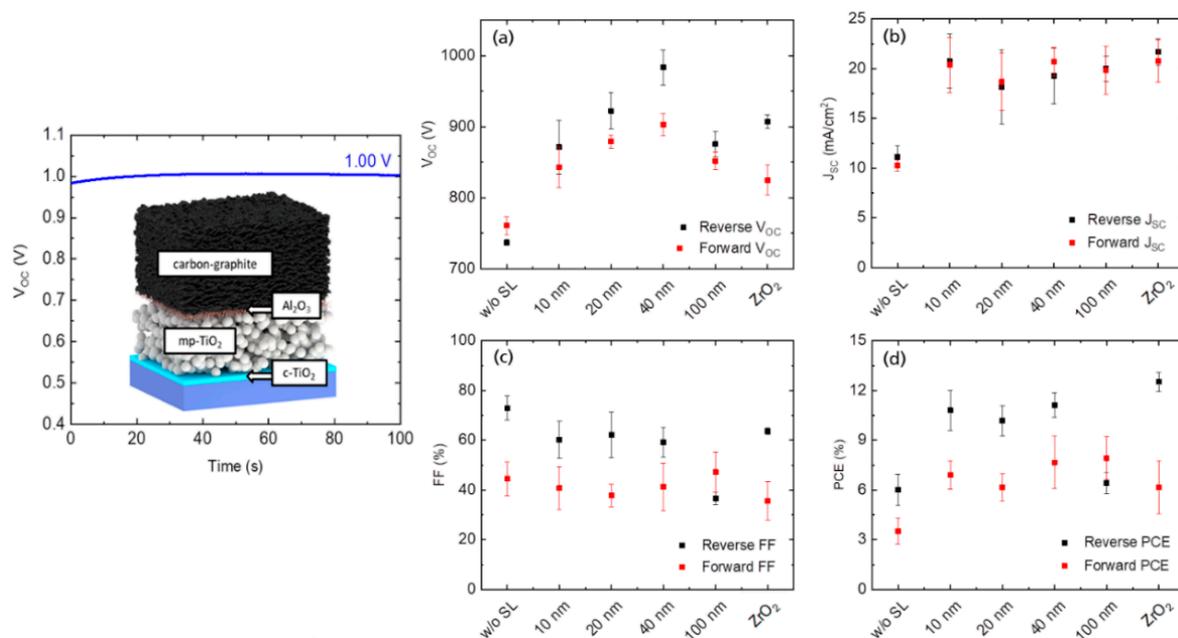


Figure 6. PV parameters of devices with sputtered Al_2O_3 insulator compared to devices with standard screen-printed ZrO_2 layers. The statistical PV parameters for each group of devices is shown in (a–d). Adopted with permission from reference [60]. Further permissions related to the material excerpted should be directed to the ACS (<https://pubs.acs.org/doi/10.1021/acsnm.9b02563>).

Insulating materials can also be combined. For example, in 2018 Han and co-workers tested 0–20 nm Al_2O_3 layers in combination with mesoporous screen-printed ZrO_2 to improve AVA-MAPbI_3 mCPSCs [61]. The Al_2O_3 was deposited via evaporation of Al onto the mesoporous TiO_2 layer and subsequent ambient annealing at 500 $^\circ\text{C}$. Using both 10 nm Al_2O_3 and screen-printed ZrO_2 layers, performance improved to 14% (from 12% without Al_2O_3) due to increased V_{oc} .

Alternatively, Al_2O_3 layers of <2.4 nm can be deposited onto the mesoporous TiO_2 via spray pyrolysis of aluminium isopropoxide before ZrO_2 screen printing [54]. In this work ZrO_2 layer thickness was optimised at 3 μm without Al_2O_3 . However, 1.2 μm exhibited

similar performance when used in conjunction with Al_2O_3 . As observed in [61], the Al_2O_3 layer improved V_{oc} , with devices exhibiting around 840 mV for a 1.2 μm ZrO_2 layer, 890 mV with the Al_2O_3 interlayer and 1.2 μm ZrO_2 and 920 mV with 3 μm ZrO_2 [54].

Al_2O_3 can be also deposited as a mesoporous layer in substitution to ZrO_2 , reported to produce an 8% PCE $\text{TiO}_2/\text{Al}_2\text{O}_3/\text{carbon-MAPbI}_3$ mCPSC by Xu et al. in 2014 [62]. PCE in these devices was then enhanced by adding p-type dopants or additional layers. For example, the introduction of an NiO interlayer between Al_2O_3 and carbon resulted in significant PCE improvement in MAPbI_3 devices (15.03% PCE from 11.2% without NiO) [63], $\text{FA}_{0.4}\text{MA}_{0.6}\text{PbI}_{2.8}\text{Br}_{0.2}$ devices (14.88% PCE) and $\text{Cs}_{0.05}(\text{FA}_{0.4}\text{MA}_{0.6})_{0.95}\text{PbI}_{2.8}\text{Br}_{0.2}$ devices (17.02% PCE) [58]. Al_2O_3 has also been used in combination with CNTs in a NiO matrix top electrode [64].

The best choice of oxide for the spacer layer may depend on the chosen perovskite formulation. The AVA- MAPbI_3 perovskite for example, is rarely used in combination with Al_2O_3 , and comparative a study for AVA- MAPbI_3 devices has shown that ZrO_2 significantly outperforms Al_2O_3 (8.72% over 5.48% PCE) [65]. Some evidence suggests that complex phase dynamics at the $\text{TiO}_2/\text{ZrO}_2$ interface can contribute significantly to AVA- MAPbI_3 device performance, which may explain this tendency [66]. Further research is therefore required on whether AVA- MAPbI_3 can produce efficient devices in an Al_2O_3 stack.

An alternative spacer layer to the more conventional ZrO_2 and Al_2O_3 is SiO_2 . This has proven to be a functional scaffold when mixed with TiO_2 in carbon-based sandwich PSCs [67]. In AVA- MAPbI_3 mCPSCs, SiO_2 spacer layers have been used to produce a 13.1% PCE device [68].

The spacer layer is the main novelty of the mCPSC architecture and introduces many interesting properties, from phase formation to the analysis of long-range charge transport. Its specific role in charge generation and transport currently remains unclear, and the specific interactions between this layer during infiltration and device operation present interesting areas for further research.

2.3. Carbon Top Electrode

Consisting principally of graphite (G) and carbon black (CB), the thickness of the carbon top contact and ratio of constituent carbon materials affects both perovskite precursor infiltration and electrode conductivity. To minimise series resistance, the conductivity of the top layer must be as high as possible without negatively affecting infiltration. This often requires compromise, as properties which improve conductivity often impede infiltration.

For example, carbon electrode conductivity increases with thickness, which is beneficial to device fill factor (FF). Conversely, excessively thick carbon layers can hinder infiltration and negatively affect performance. The same is true for the CB:G ratio, which can also be used to tune layer conductivity and porosity. While CB can increase the layer porosity, it is less conductive than graphite. An optimum CB content in a G:CB formulation is around 20% weight [69]. The size of particle also impacts performance: it has also been found that larger G flakes increase both conductivity and porosity. The optimal size for G flakes was found to be $\sim 9 \mu\text{m}$ in an 8 μm thick layer [70].

Alternatively, this challenge can be addressed through application of an additional conductive layer to the infiltrated device. As these materials are applied directly onto the annealed perovskite, they must be carefully chosen and annealed at low temperatures so as not to damage the underlying absorber. Initial attempts saw PCE improvements from 12.6% to 14% with the addition of G flakes [71]. An alternative approach is to apply a metallic grid using a second, non-porous carbon ink that does not affect the perovskite performance [72]. The use of Al and Cu metallic grids in this way improved device performance by over 2%—from around $\sim 11\%$ to around $\sim 13\%$. Device stability was also enhanced, as the additional carbon ink limited environmental exposure of the underlying perovskite. Organic HTL have also been trialled: infiltration of the organic spiro-OMeTAD through the carbon layer of a pre-fabricated mCPSC resulted in efficiency improvements from 9% to 10%, although this significantly increased device cost [73].

Performance may also be enhanced by improving top contact selectivity: the carbon is a poor electron barrier, which can limit hole extraction. This can be improved by raising the layer work-function, usually achieved through chemical modification of the carbon. For example, functionalising with C–OH and C=O groups during carbonisation of the raw materials can raise the carbon work function from 5.0 eV to 5.2 eV [74]. The increase was considered the main contributor to observed device improvements, from 13.6% to 15.7% PCE. Similarly, boron functionalised graphite was found to raise the carbon layer work function from 4.81 eV to 5.10 eV, resulting in a performance increase from around 12.5% to 13.5% [75].

Carbon nanotubes (CNTs) have also been used to improve the hole selectivity. Incorporating hole conducting single walled CNTs (SWCNTs) has produced a MAPbI₃ device of In 2016 Wang 14.7% PCE [76]. Alternatively SWCNT can be applied as part of an NiO matrix in the top electrode [64]. A p-type material that can improve hole extraction NiO matrices resulted in PCE enhancement from 10.5% to 12.7%. Notably, the NiO devices achieved this performance with a relatively thin electrode of 1.8 μm, compared to 10 μm for control devices. This material could therefore be used to fabricate high performance devices with alternative perovskite formulations, which can struggle to infiltrate standard thick layers.

Many p type inorganic materials could be used to improve hole extraction in this way. Some examples are WO₃, CuS, MoO₃, Co₂O₃ and CuO, proposed in 2018 as potential performance enhancing additives for mCPSC carbon layers [53,77,78]. Inorganic HTLs can also be incorporated as a discrete layer between the insulating layer and carbon electrode as discussed in Section 2.2. The large body of work in improving carbon band alignment proves that compositional manipulation of this layer is a valid and effective route to performance enhancement.

2.4. Additional p-Type Materials

The mCPSC can operate efficiently without a selective HTL, which is advantageous as many PSC HTLs are fabricated with expensive and unstable organic materials such as spiro-OMeTAD. However, the poor selectivity and extreme thickness of the stack also means that device efficiencies still lag behind other PSC architectures. In mCPSCs, photo-generated holes must travel a relatively long distance through the perovskite in the insulating layer before reaching the carbon, increasing the risk of recombination.

A recent optical modelling study found that long wavelength light deeply penetrates the stack before absorption [56]. Light absorption in the depth of the device may generate electron-hole pairs close to the carbon layer, which would be at high risk of surface recombination due to the non-selectivity of the carbon. Incorporating hole-selective materials could therefore significantly improve performance by reducing recombination.

Inorganic NiO appears to be the most promising HTL for mCPSCs: this intrinsic p-type semiconductor has high optical transmittance, a deep-lying valence band (~5.4 eV) and good thermal stability. This material has been incorporated into mCPSCs in several ways. Liu et al. presented a fully printable mCPSC with crystalline NiO nanosheets as a HTL between the ZrO₂ and carbon layers [79]. The NiO nanosheets provided superior charge collection and prolonged charge lifetime, resulting in a PCE of 14.2%. Xu et al. also inserted a 480 nm NiO p-type interlayer between the insulating layer and the carbon electrode [80]. The NiO interlayer enhanced the device performance to 14.9% PCE by improving hole extraction, raising device V_{oc} and FF.

NiO interlayers have also been investigated with Al₂O₃ spacers [63]. The fully printable TiO₂/Al₂O₃/NiO/carbon-MAPbI₃ device achieved 15.03% PCE, much higher than the 11.2% standard device (Figure 7). High IPCE and improved FF indicated that the NiO layer (800 nm in this case) improved both charge transport and collection. The performance was further improved when the device was infiltrated with a triple cation perovskite of Cs_{0.05}(FA_{0.4}MA_{0.6})_{0.95}PbI_{2.8}Br_{0.2}; incorporating Cs⁺ increased the bandgap and the exciton binding energy, which is beneficial to hole extraction [58]. The Cs_{0.05}(FA_{0.4}MA_{0.6})_{0.95}PbI_{2.8}Br_{0.2} based NiO device achieved a PCE of 17.02%, which represents one of the highest recorded efficiencies so far for mCPSCs.

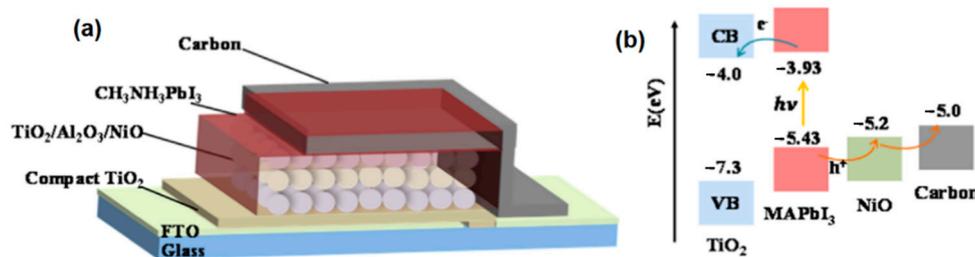


Figure 7. (a) Schematic representation of the $\text{TiO}_2/\text{Al}_2\text{O}_3/\text{NiO}/\text{carbon}$ device infiltrated with $\text{CH}_3\text{NH}_3\text{PbI}_3$ (MAPbI₃); (b) Energy band diagram of the fabricated device configuration. Reproduced with permission from reference [63].

Ultraviolet (UV/O₃) treatments can enhance the properties of NiO HTLs by oxidising Ni^{2+} to Ni^{3+} on the layer surface, affecting the electronic distribution of the semiconductor. Fabricated mCPSCs had improved charge extraction attaining a PCE of 12.1% with a 450 nm thick Al_2O_3 insulating layer [81].

Despite the advantages of this material, NiO-based mCPSCs are still not performance optimised due to the high series resistance (R_s) of NiO films, a consequence of low intrinsic conductivity. This can be improved through partial doping with metals such as copper (Cu), which mitigates associated FF and J_{sc} losses through increasing the p-type conductivity of the layer. In mCPSCs, Cu:NiO_x nanoparticles can improve J_{sc} and FF values through increasing hole extraction efficiency [37]. Single cells and 100 cm² modules (active area 70 cm²) with an 80 nm mesoporous Cu:NiO_x layer have been fabricated, with champion PCEs of 12.1% compared to 9.75% for HTL free samples. The single cells were only slightly better than the modules: 12.8% with Cu:NiO_x, 10.49 with NiO and 9.21% without HTL. This proves that mCPSC up-scaling can be achieved without significant performance loss.

An inorganic hole transporting interlayer of spinel Co_3O_4 has also been investigated as a recombination suppressing HTL for mCPSCs [82]. Devices with 70 nm screen printed Co_3O_4 interlayers exhibited PCEs of 13.27%, an 18% efficiency enhancement on standard devices. This is attributed to the lower hole charge transfer resistance and reduced recombination at the interface, as proven by photoluminescence (PL) and time resolved PL (TRPL) spectroscopy.

Inorganic HTLs can be also introduced into the carbon ink before printing. Due to its appropriately positioned conduction band (5.2 eV), WO_3 nanoparticles have been incorporated into the carbon electrode to promote hole-extraction at the perovskite/carbon interface [78]. Results of electrochemical impedance spectroscopy (EIS), PL and TRPL spectra revealed that the carbon electrode with WO_3 exhibited a more efficient charge transport and interfacial transfer than WO_3 -free devices. This led to a cell with 10.77% PCE, a 40% increase over reference devices. Other materials have been successful in improving the performance in other architectures: for example, 1% CuS doping of carbon pastes for conventional sandwich carbon-based devices (i.e., not mCPSC) increased the average PCE from 8.41% to 10.22% [77]. Scope clearly exists for applying these methods to mCPSCs.

A noteworthy example of an alternative method for introducing HTLs incorporated CuSCN into the perovskite precursor before infiltration [83]. This can be challenging, as adding an HTL into the perovskite solution can negatively impact on its rheological properties, which may cause infiltration issues and incomplete pore filling. In this example, a fully printed $\text{TiO}_2/\text{ZrO}_2/\text{carbon}$ device infiltrated with a AVA-MAPbI₃ with 4 mol% CuSCN showed faster charge extraction, reduced hysteresis and a small improvement in average PCE (from 14% to 14.3%).

A summary of the reported results, materials and techniques reported for devices using MAPbI₃ or AVA-MAPbI₃ perovskites is offered in Table 1.

Table 1. Summary of reported with information about materials, processing and PCE values. All the devices were prepared on FTO glass (F:SnO₂). Film deposition methods are: ⁽¹⁾ spray pyrolysis, ⁽²⁾ screen-printing, ⁽³⁾ spin coating, ⁽⁴⁾ sputtering, ⁽⁵⁾ vacuum evaporation, ⁽⁶⁾ doctor-blade, ⁽⁷⁾ inkjet. Infiltration methods: ^(a) drop casting, ^(b) spin coating, ^(c) Inkjet, ^(d) RbM. Only optimal performance is reported. When a range of thickness is reported, the table refer to an average value.

Ref	Inorganic Stack Materials, Deposition Method and Thickness	Perovskite Formulation	PCE (%) Illumination Area (mm ²), Publication Year	V _{oc} (mV) J _{sc} (mA/cm ²) FF (%)	Comments
[14]	TiO ₂ ⁽¹⁾ (100 nm)/TiO ₂ ⁽²⁾ (1 μm)/ ZrO ₂ ⁽²⁾ (1 μm)/C ⁽²⁾ (10 μm)	MAPbI ₃ , one step ^(a)	6.64 (12.5) 2013	878/12.4/61	First report on mCPSC
[12]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽²⁾ (0.6 μm)/ ZrO ₂ ⁽²⁾ (1 μm)/C ⁽²⁾ (10 μm)	MAPbI ₃ , two steps ^(b)	10.64 (13) 2014	868/20.1/61	TiO ₂ in nano-sheets
[25]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽²⁾ (1 μm)/ ZrO ₂ ⁽²⁾ (2 μm)/C ⁽²⁾ (10 μm)	AVA-MAPbI ₃ , one step ^(a)	11.6 (7) 2014	843/21.1/65	First report on AVA-MAPbI ₃
[70]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽²⁾ (0.4 μm)/ ZrO ₂ ⁽²⁾ (0.5 μm)/C ⁽²⁾ (9 μm)	MAPbI ₃ , two steps ^(a)	11.6 2014	894/18.1/72	
[69]	TiO ₂ ⁽¹⁾ (100 nm)/TiO ₂ ⁽²⁾ (1 μm)/ ZrO ₂ ⁽²⁾ (1 μm)/C ⁽²⁾ (5 μm)	MAPbI ₃ , one step ^(a)	7.08 (13) 2015	867/15.2/54	
[52]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽²⁾ /ZrO ₂ ⁽²⁾ /C ⁽²⁾	MAPbI ₃ , either one or two steps ^(a)	12.3 2015	921/18.3/73	
[63]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽²⁾ (0.4 μm)/ Al ₂ O ₃ ⁽²⁾ (0.4 μm)/NiO ⁽²⁾ (0.8 μm)/C ⁽⁶⁾ (10 μm)	MAPbI ₃ , two steps ^(a)	15.0 (16) 2015	915/21.6/76	First mCPSC with NiO interlayer
[80]	TiO ₂ /TiO ₂ ⁽⁷⁾ /ZrO ₂ ⁽⁷⁾ /NiO ⁽⁷⁾ /C ⁽⁷⁾	MAPbI ₃ , two steps	14.9 2015	917/21.4/76	
[76]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽²⁾ (0.4 μm)/ Al ₂ O ₃ ⁽²⁾ (0.65 μm)/C ⁽²⁾ (10 μm)	MAPbI ₃ , two steps ^(a)	14.7 (19.6) 2016	1010/21.3/69	SWCNT in the carbon electrode
[15]	TiO ₂ ⁽¹⁾ (50 nm)/TiO ₂ ⁽²⁾ (0.6 μm)/ ZrO ₂ ⁽²⁾ (1.5 μm)/C ⁽²⁾ (8 μm)	MAPbI ₃ , two steps ^(a)	11.3 (6.5) 2017	780/21.9/66	NIR-fast annealing for the inorganic stack
[58]	TiO ₂ /TiO ₂ ⁽²⁾ /Al ₂ O ₃ ⁽²⁾ /NiO ⁽²⁾ /C ⁽²⁾	Cs _x (FA _{0.4} MA _{0.6}) _{1-x} PbI _{2.8} Br _{0.2} , two step ^(a)	17.02 (12.9) 2017	1008/23.4/72	Highest performance so far
[34]	TiO ₂ ⁽¹⁾ (35 nm)/TiO ₂ ⁽²⁾ (0.5 μm)/ ZrO ₂ ⁽²⁾ (2 μm)/C ⁽²⁾	AVA-MAPbI ₃ , one step ^(c)	9.53 (16) 2017	861/16.6/67	Inkjet to infiltrate the perovskite solution
[71]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽²⁾ /ZrO ₂ ⁽²⁾ /C ⁽²⁾	AVA-MAPbI ₃ , one step ^(a)	14.1 2017	901/23.0/68	Use of ultra-thin graphite
[75]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽²⁾ /ZrO ₂ ⁽²⁾ /C ⁽²⁾	AVA-MAPbI ₃ , one step ^(a)	13.6 2017	940/22.9/63	Boron-doped graphite
[64]	TiO ₂ ⁽¹⁾ (50 nm)/TiO ₂ ⁽²⁾ (0.45 μm)/ Al ₂ O ₃ ⁽²⁾ (0.9 μm)/NiO-SWCNT ⁽²⁾ (1.8 μm)	MAPbI ₃ , two steps ^(a)	12.7 (20) 2017	945/20.7/64	NiO/SWCNT composite as top electrode
[46]	TiO ₂ ⁽²⁾ /ZrO ₂ ⁽²⁾ (2.1 μm)/C ⁽²⁾ (13 μm)	AVA-MAPbI ₃ , one step ^(a)	9.11 2017	880/23.5/44	Double mesoscopic stack
[81]	TiO ₂ ⁽¹⁾ (50 nm)/TiO ₂ ⁽²⁾ / Al ₂ O ₃ ⁽²⁾ (0.45 μm) ⁽²⁾ /NiO _x ⁽²⁾ /C ⁽²⁾ (5 μm)	MAPbI ₃ , two steps ^(a)	11.3 2017	945/17.2/69	O ₃ treatment on to oxidise Ni ²⁺ → Ni ³⁺ for NiO _x layer
[36]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽²⁾ /ZrO ₂ ⁽²⁾ /C ⁽²⁾	MAPbI ₃ , two steps ^(d)	10.4 (9) 2018	802/19.9/65	RbM deposition for PbI ₂

Table 1. Conts.

Ref	Inorganic Stack Materials, Deposition Method and Thickness	Perovskite Formulation	PCE (%) Illumination Area (mm ²), Publication Year	V _{oc} (mV) J _{sc} (mA/cm ²) FF (%)	Comments
[74]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽²⁾ (0.8 μm)/ ZrO ₂ ⁽²⁾ (1.2 μm)/C ⁽²⁾ (10 μm)	AVA-MAPbI ₃ , one step ^(a)	15.7 2018	980/23.2/69	Oxygen-rich carbon electrode
[53]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽²⁾ (0.5 μm)/ ZrO ₂ ⁽²⁾ /C ⁽²⁾ (10 μm)/c- C ⁽²⁾ (20 μm)	AVA-MAPbI ₃ , one step ^(a)	13.9 (10.2) 2018	920/23.4/65	Compact carbon layer with p-type doping on the top
[54]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽²⁾ (0.5 μm)/Al ₂ O ₃ ⁽¹⁾ (<2.4 nm) ZrO ₂ ⁽²⁾ (1.2 μm)/C ⁽²⁾ (15 μm)	AVA-MAPbI ₃ , one step ^(a)	14.4 (10.8) 2018	900/22.8/70	
[61]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽²⁾ /Al ₂ O ₃ ⁽⁵⁾ (10 nm)/ ZrO ₂ ⁽²⁾ (1 μm)/C ⁽²⁾	AVA-MAPbI ₃ , one step ^(a)	14.3 (10.7) 2018	930/23.3/66	
[68]	TiO ₂ ⁽³⁾ /TiO ₂ ⁽³⁾ /SiO ₂ ⁽³⁾ /C ⁽⁶⁾	AVA-MAPbI ₃ , one step ^(a)	13.1 2018	900/22.6/64	
[82]	TiO ₂ ⁽²⁾ /TiO ₂ ⁽²⁾ /ZrO ₂ ⁽²⁾ /Co ₃ O ₄ ⁽²⁾ (70 nm)/C ⁽²⁾	AVA-MAPbI ₃ , one step ^(a)	13.3 (9) 2018	880/23.4/64	
[37]	TiO ₂ ⁽²⁾ (50 nm)/TiO ₂ ⁽²⁾ (0.5 μm)/ZrO ₂ ⁽²⁾ (1.4 μm)/Cu:NiO _x ⁽²⁾ (80 nm)/C ⁽²⁾ (13 μm)	AVA-MAPbI ₃ , one step ^(a)	12.79 (80) 2019	1000/22.2/57	12.1% on 70 cm ² module
[21]	TiO ₂ /TiO ₂ ⁽²⁾ /ZrO ₂ ⁽²⁾ /C ⁽²⁾ (12 μm)	AVA-MAPbI ₃ , one step ^(a)	13.3 (16.0) 2019	900/21.4/69	
[78]	TiO ₂ ⁽³⁾ /TiO ₂ ⁽³⁾ /Al ₂ O ₃ ⁽³⁾ /C ⁽²⁾	AVA-MAPbI ₃ , one step ^(a,b)	10.8 (30) 2019	934/18.0/64	WO ₃ in the carbon layer
[55]	TiO ₂ ⁽¹⁾ (50 nm)/TiO ₂ ⁽²⁾ (1 μm)/ZrO ₂ ⁽²⁾ / C ⁽²⁾ (15 μm)	AVA-MAPbI ₃ , one step ^(a)	11.9 (13) 2019	871/19.1/71	Optimisation of the ZrO ₂ layer
[59]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽²⁾ (1 μm)/Al ₂ O ₃ ⁽²⁾ (1.2 μm)/ C ⁽²⁾ (15 μm)	MAPbI ₃ , two steps ^(b)	10.7 (16) 2019	870/19.3/64	In presence of water
[47]	TiO ₂ ⁽³⁾ /TiO ₂ ⁽²⁾ /ZrO ₂ ⁽²⁾ /C ⁽²⁾	AVA-MAPbI ₃ , one step ^(a)	10.81 (10) 2019	910/19.1/62	Dual-sized TiO ₂ particles
[79]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽²⁾ (0.45 μm)/ZrO ₂ ⁽²⁾ (0.4 μm)/ NiO ⁽²⁾ (1 μm)/C ⁽²⁾ (8 μm)	MAPbI ₃ , two steps ^(a)	14.2 2019	965/20.4/72	Use of NiO nanosheets
[83]	TiO ₂ /TiO ₂ /ZrO ₂ /C	AVA-MAPbI ₃ , one step ^(a)	14.6 (16) 2019	891/21.9/75	CuSCN in the perovskite formulation
[73]	TiO ₂ ⁽³⁾ /TiO ₂ ⁽²⁾ (450 nm)/ ZrO ₂ ⁽²⁾ (1.7 μm)/C ⁽²⁾ (25 μm)	AVA-MAPbI ₃ , one step ^(a)	10.0 (10) 2019	890/21.7/52	Spiro-OMeTAD spin coated on the top
[72]	TiO ₂ ⁽¹⁾ (50 nm)/TiO ₂ ⁽²⁾ (0.6 μm)/ ZrO ₂ ⁽²⁾ (1.5 μm)/C ⁽²⁾ (13 μm)	AVA-MAPbI ₃ , one step ^(a)	13.2 (49) 2020	810/23.8/68	Laminated Cu-grid
[57]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽²⁾ (0.5 μm)/ ZrO ₂ ⁽²⁾ (0.75 μm)/C ⁽²⁾ (15 μm)	Cs _{0.1} Rb _{0.05} FA _{0.85} PbI ₃ , one step ^(a)	16.2 (10.8) 2020	909/22.7/79	
[60]	TiO ₂ ⁽⁴⁾ (20 nm)/TiO ₂ ⁽²⁾ (1.5 μm)/ Al ₂ O ₃ ⁽⁴⁾ (40 nm)/C ⁽²⁾ (8 μm)	AVA-MAPbI ₃ , one step ^(a)	11.1 (40) 2020	983/19.3/59	Very thin spacer layer
[48]	TiO ₂ ⁽³⁾ /TiO ₂ +EuW ₁₀ ⁽²⁾ /ZrO ₂ ⁽²⁾ /C ⁽²⁾	AVA-MAPbI ₃ , one step ^(a)	14.36 (10) 2020	880/23.5/72	Optimisation of the EuW ₁₀ :TiO ₂ ratio
[35]	TiO ₂ ⁽⁷⁾ /TiO ₂ ⁽⁷⁾ /ZrO ₂ ⁽⁷⁾ /C ⁽²⁾	AVA-MAPbI ₃ , one step ^(c)	9.1 (64) 2020	900/17.2/59	

3. Modifications to the Perovskite Formulation

3.1. Perovskite Requirements and Infiltration Techniques

As a relatively new and structurally unique architecture, the perovskite requirements of mCPSCs are perhaps less well-understood than those of conventional PSCs. These devices are orders of magnitude thicker than conventional structures and the perovskite is entirely contained within a mesoporous scaffold, meaning no perovskite capping layer is present. This significantly changes the required perovskite properties. In non-mCPSC device structures many additives and deposition techniques for the performance and stability enhancement of conventional sandwich PSCs focus on increasing the grain size, coverage and crystallinity of this capping layer [84–87].

In mCPSCs, grain size is restricted to that of the scaffold pores. Fast crystallisation to form large grains improves the performance and stability of sandwich PSCs by maximising absorption and decreasing the number of unstable grain boundaries. However, such crystallisation behaviour can be detrimental to mCPSC performance, as crystals can preferentially form above the scaffold or impede infiltration by producing blockages [47,65,88]. Instead, infiltration (the extent to which precursor permeates the stack) becomes a major concern. As the inorganic stack is relatively thick, in the order of micrometres, and the carbon layer is made of large graphite flakes, the infiltration is non-trivial [88]. Devices with poor infiltration are less stable, and exhibit low J_{sc} and V_{oc} values due to poor interfacial contact and reduced charge collection [88]. The full device stack is usually in excess of 15 μm , and a high performing device requires complete permeation of all three layers with minimal voids. To achieve this, crystallisation must be relatively slow and carefully controlled, rendering common methods such as antisolvent assisted spin coating unsuitable.

To ensure complete infiltration, mCPSC annealing temperatures tend to be far lower than in conventional PSCs, generally ranging from 50 °C–100 °C depending on the perovskite composition [25,89,90]. Coupled with the grain-size restrictions introduced by the scaffold, such long, low temperature processes can lead to unfavourable phase formation; for example, FAPbI_3 tends to form the yellow δ -phase in this temperature range [91]. These problems may be addressed by changing processing methods and precursor formulations, as discussed in Section 3.4 [90,92–94].

Perovskite morphology and crystallisation dynamics may be influenced during and after annealing through changing process parameters and applying post-crystallisation treatments. Most mCPSC infiltration is performed via drop-casting of perovskite precursors. This can be done using one or two-step methods. A one-step method involves application of a stoichiometric ABX_3 solution in γ -butyrolactone (GBL) or in a solution of N,N -dimethylformamide and dimethyl sulfoxide (DMF/DMSO), which directly forms the desired perovskite on solvent removal. In a two-step method, the lead halide (usually in DMF) is infiltrated into the stack and annealed before subsequent reaction with the solvated organic cation in a 2-propanol (IPA) chemical bath (Figure 8).

One-step methods are easier to scale up and involve fewer reagents, advantageous especially when using harmful solvent systems. However, reproducible, high quality infiltration is often more difficult to obtain than with a two-step technique [88] (Figure 8). This can be overcome by using vapour assisted annealing techniques, or additive and solvent engineering, as discussed in the following Section [25,57,90].

Two broad approaches may be taken for chemical modification of the perovskite itself: substitution of component perovskite ions or the introduction of additives. Whereas substitution refers to an ion that sits within the bulk ABX_3 lattice framework, additives are either removed during annealing or sit at interfaces or in interstitial sites. The terms are often used interchangeably, possibly because some species can act in both ways, with some of the added material incorporating into the lattice and the rest collecting at grain boundaries and other interfaces [95]. Other molecules, such as AVA, partially integrate into the lattice at grain boundaries via functional groups [27]. These species, as well as those able to act as both additives and substituents will be included in the ‘additives’ section of this review.

A variety of additives and substituents have been successfully incorporated into mCPSC in recent years using one and two-step methods. The following section will highlight these developments to give a detailed overview of advancements in the field.

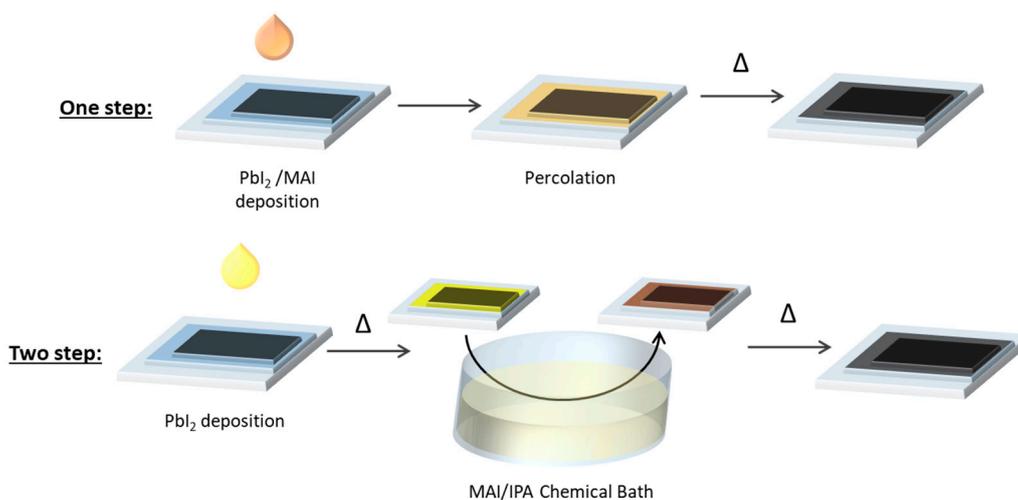


Figure 8. Diagrammatic representation of infiltration in a typical MAPbI_3 /dimethylformamide (DMF) one-step (above) and two-step (below) method.

3.2. Organic Additives

By far the most conspicuous additive is 5-aminovaleric acid (AVA). This molecule can be added in both molar ratio and excess and has been found to improve performance in many distinct ways.

First presented as a perovskite precursor component in 2014 to improve infiltration, cross-sectional analysis has since shown that 3% AVA in a MAPbI_3 -GBL precursor can improve infiltration by 94% when applied in a one-step deposition (Figure 9). This is achieved by a templating effect, where AVA adsorbs to the metal oxide surface via carboxyl groups, improving precursor wetting and introducing nucleation points. As shown by large increases in IPCE spectra, the small, densely packed crystals observed using transmission electron microscopy (TEM) enhance light absorption and charge extraction at the perovskite-titania interface, resulting in high J_{sc} and corresponding PCE improvement [25,88].

Better scaffold infiltration can enhance stability through restricting the movement of species such as gaseous methylammonium iodide (MAI) in and out of devices, with MAPbI_3 mCPSCs exhibiting longer lifetimes under ambient conditions than classic mesoscopic devices [14]. Similarly, well infiltrated MAPbI_3 mCPSCs produced by two-step infiltrations are more resilient to degradation than poorly infiltrated cells [88]. However, the extent to which AVA enhances device stability is beyond that usually observed with improved infiltration. The light-exposed lifetime of unencapsulated MAPbI_3 devices increases by a factor of 40 with 3–5% AVA addition, extending the time taken for 50% initial performance loss from ~ 2 to ~ 80 h [96]. In other works, glass-encapsulated devices exposed to continuous light at 55°C lost no performance over $>10,000$ h, and AVA- MAPbI_3 mCPSCs have recently passed IEC2016 qualifications with 9000 h operational tracking—unprecedented in other device architectures [27,28]. More detailed information on the lifetime and stability tests carried out on these devices is available in Section 4.

Multiple factors contribute to this lifetime enhancement. In 2016, it was posited that the templating and crosslinking effects of AVA also lead to the formation of a 2D/3D gradation at the perovskite-titania interface, evidenced by a blue-shift in AVA- MAPbI_3 PL (photoluminescence) and downshifting of x-ray diffractogram (XRD) peaks when in the presence of TiO_2 . The formed layer enhances stability by protecting grain boundaries from moisture and improves charge collection through acting as a barrier to electron recombination. Unlike in other perovskite formulations, some humidity exposure is beneficial for

AVA-MAPbI₃ mCPSCs. Devices exposed to ~70% RH for 24–28 h show significant PCE enhancement and significantly reduced hysteresis caused by improved crystallinity [27,97]. Water-mediated performance improvements in the absence of stability decreases are highly unusual and appear unique to the AVA-MAPbI₃ mCPSC. The resistance to water induced degradation is likely a consequence of the 2D AVA-MAPbI₃ capping layer, which has a low defect concentration. Additionally, the large organic AVA tails provide a physical barrier, preventing water molecules accessing surface defects and inhibiting MAI loss and irreversible ion migration [28].

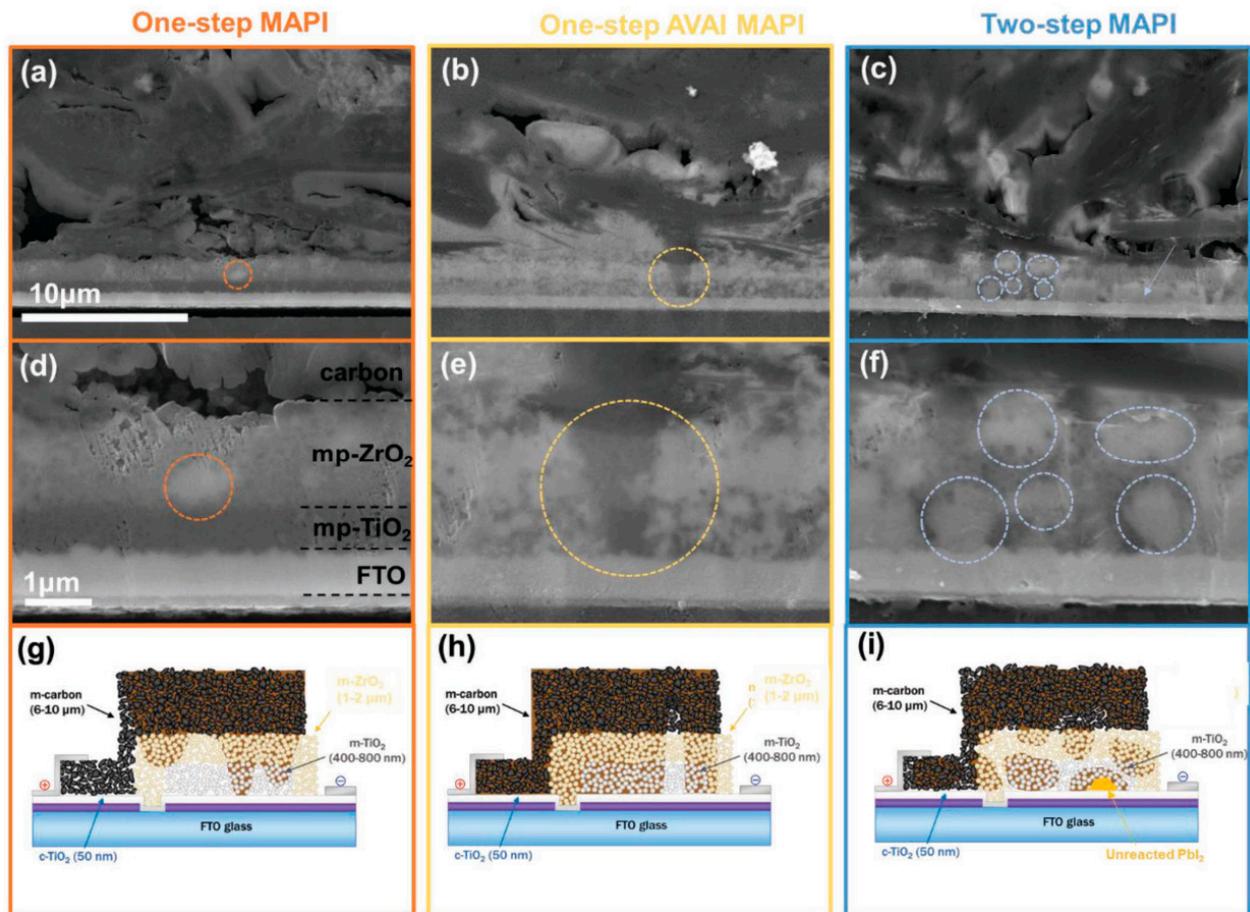


Figure 9. High and low magnification cross-sectional secondary electron microscopy (SEM) images of mesoscopic perovskite solar cells based on (a,d) one-step MAPbI₃, (b,e) one-step AVAI-MAPbI₃, and (c,f) two-step MAPbI₃. Cartoon showing mesoscopic perovskite solar cell structure and perovskite infiltration using (g) one-step MAPbI₃, (h) one-step AVAI-MAPbI₃ or (i) two-step MAPbI₃ processes. Reproduced with permission from reference [88].

However, other stability enhancing mechanisms are also present: this 2D phase formation only occurs in the presence of a metal oxide, and AVA-MAPbI₃ films have demonstrated unusual stability in the absence of such materials. It has been found that AVA addition also significantly inhibits superoxide mediated degradation: despite the small grains in the glass-based AVA-MAPbI₃ films resulting in more superoxide generation than analogous MAPbI₃ layers, they exhibit far greater stability towards photo-induced oxygen degradation. This effect is attributed to defect passivation at grain surfaces, which blocks superoxide driven degradation at boundaries by preventing access to surface defects. In mCPSCs, AVA-MAPbI₃ perovskites produce less superoxide under ambient conditions than MAPbI₃ as the high-quality scaffold infiltration limits oxygen access to grain surfaces. An additive stabilisation effect is therefore at work in mCPSCs, wherein superoxide is less easily generated and, when present, less able to induce perovskite degradation [96,97].

AVA contributes to mCPSC performance enhancement in so many ways that it is perhaps unsurprising that an equally effective alternative has yet to emerge. Although several organic additives have appeared in the literature, the combination of performance enhancement and lifetime extension is hard to match, and none have had the same lasting impact. The physical and chemical structure of AVA is intrinsic to its performance-enhancing properties. The carboxyl is key to both the templating effect and the formation of 2D/3D phases: structurally similar additives lacking this group, such as benzylamine do not improve infiltration and form single phases [98] (Figure 10). Additionally, the aliphatic chain is long enough to prevent interfacial recombination, short enough not to act as a barrier to electron transport, and lacks disorganised side chains that can introduce crystal growth variation [27]. Even small changes in the aliphatic chain length can result in poor devices: despite possessing the same functional groups as AVA, recent work found shorter amino acid DL-alanine to be ineffective in improving device performance [99].

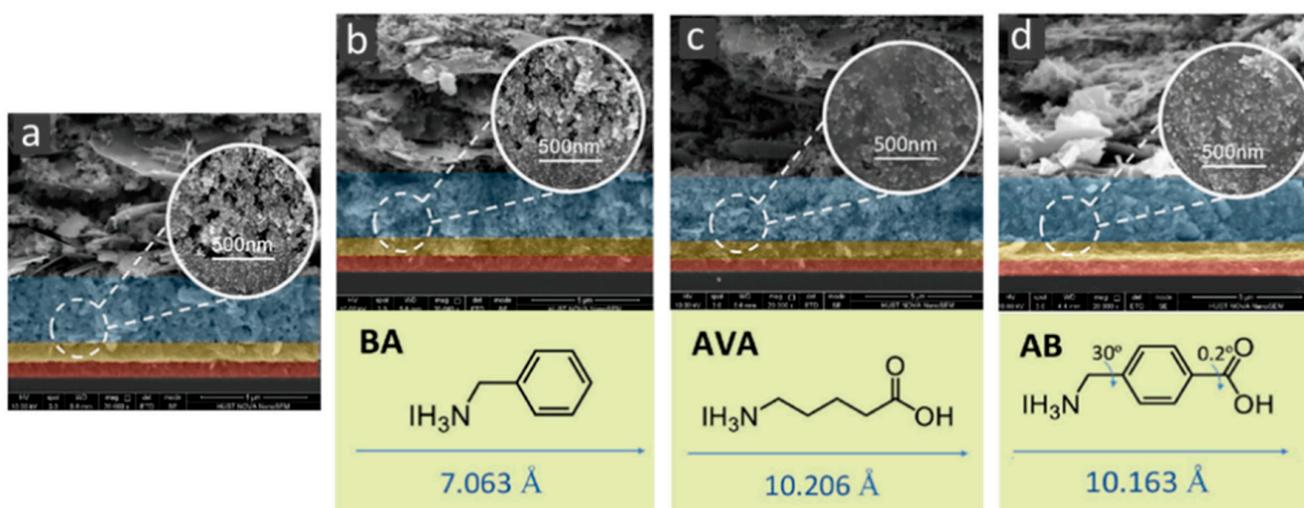


Figure 10. Cross-sectional SEM images of mCPSCs filled with (a) MAPbI₃, (b) BA-MAPbI₃, (c) AVA-MAPbI₃, and (d) AB-MAPbI₃ with molecular structures of the additives. Adapted with permission from reference [98].

The most effective direct alternatives are therefore bifunctional molecules of similar size and structure, such as other amino acids or conjugated amino-carboxylic acids. Perhaps the most effective direct replacement thus far is 4-(aminomethyl) benzoic acid hydroiodide (AB), a conjugated molecule with the same functional groups and similar length to AVA (Figure 10). Fabricated devices showed enhanced carrier lifetime and thus higher PCE than those using AVA, with an average of ~15.0% PCE compared to ~13.5%. However, AB did not improve device lifetimes to the same extent as AVA, losing over 10% PCE over 100 h of light soaking. This may explain the continued use of AVA for scale-up projects and in standard benchmark devices [31,98].

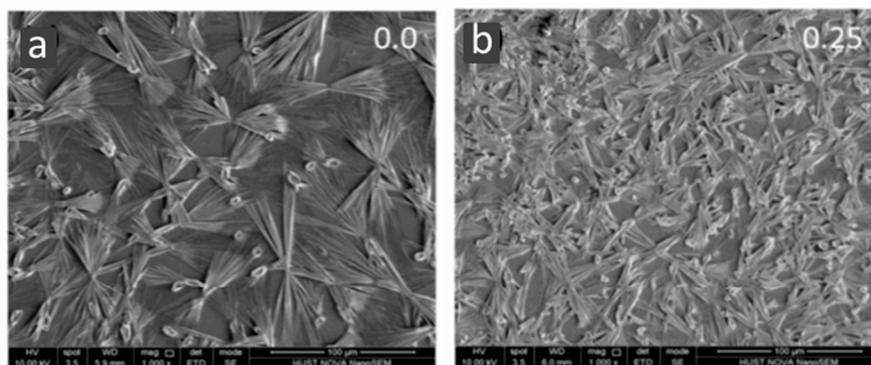
Infiltration and crystal packing density in mCPSCs can also be enhanced using molecules such as thiourea, which direct crystal growth by forming hydrogen bonds in the precursor solution and between growing crystals during annealing. This causes the additive to act as a crystal growth inhibitor at low concentration, while promoting nucleation and directional growth at high concentration. The resultant film is closely packed and well infiltrated, with thiourea acting as a crosslinker between adjacent grains in the scaffold. Both device performance and lifetime were enhanced, with PCEs rising from 9.45% to 12.07% and thiourea mCPSCs maintaining >90% PCE over 1000 h of dark storage. It was suggested that lifetime improvements were a consequence of thiourea catalysed I₂ → 2I⁻ reduction, a different mechanism of action to AVA. Combined use with AVA or other surface passivating additives could therefore offer avenues into further lifetime enhancement [100]. A recent example of such work showed that adding phenethylammonium Iodide (PEAI) reduced PCE loss in AVA_{0.03}MAPbI₃ devices stored in ambient conditions,

although it should be noted that the devices in this study were of low PCE to begin with (PCE 5.95%) [101].

Guanidinium (Gu), and analogues improve MAPbI₃ scaffold contact and infiltration in mCPSCs, through similar crosslinking and hydrogen bonding-mediated changes in crystallisation dynamics. An initial study examining GuCl as an additive in mCPSCs produced average PCEs of 13.25% compared to 8.14% for MAPbI₃ when using a one-step DMF-based deposition [102]. A follow-up study in 2020 expanded on these findings, comparing one-step DMF/DMSO based depositions doped with GuCl, biguanide hydrochloride (BCl) and dimethylbiguanide hydrochloride (DCI). A 30% molar addition of BCl resulted in a champion mCPSC of 16.35% PCE compared to a control device of 9.65% due to highly oriented crystal growth and packing and a favourable shift in perovskite band edges [103].

Interestingly, the above additives result in a similar crystal profile to AVA films, with small, densely packed, round crystal structures throughout the stack. The changes in crystal shape indicate that additives are influencing both nucleation and crystal growth dynamics [98].

Other mCPSC additives such as PCBM ([6,6]-phenyl-C61-butyric acid methyl ester) and carbon quantum dots, solely influence nucleation. Increasing the number of nucleation points effectively reduces crystal size without affecting shape (Figure 11). With little effect on crystal growth dynamics post-nucleation, there is less risk of these additives resulting in unwanted phase formation [104,105]. Simply decreasing crystallite dimensions in this way can drastically improve mCPSC infiltration, J_{sc} and PCE. For example, when 0.25 mg/mL of PCBM added to one-step DMF MAPbI₃ precursors, PCEs of over 11% were obtained: a similar PCE improvement to that observed in the first GBL/AVA trials [104].



[PCBM] (mg/ml)	V_{oc} (V)	J_{sc} (mAc m^{-2})	FF	PCE (%)
0	0.91±0.04	12.85±1.45	0.63±0.04	7.39±0.84
0.25	0.94±0.02	19.04±1.13	0.64±0.01	11.49±0.65

Figure 11. MAPI on FTO with (a) 0 mg/mL and (b) 0.25 mg/mL PCBM, with corresponding average mCPSC parameters. Adapted from [85].

Organic additives may alternatively be incorporated through pre-treatment of the mesoporous stack prior to infiltration. In 2015, an organosilane self-assembled monolayer (SAM) was applied to stacks via chemical bath deposition. Arranged on the titania surface, the applied SAMs improved the perovskite/ETL band alignment. When applied in MAPbI₃ mCPSCs infiltrated via a two-step method, average device performance went from 9.6% to 11.7% PCE due to significant V_{oc} and FF enhancements [106].

Similarly, a recent study example examined the effect of pre-treating the mesoscopic stack with alkali metal salts of 2-phenyl-5-benzimidazole. The salts had a dual effect on performance: while the organic component acted to improve wetting and interfacial stack/perovskite contact, changing the metal ion impacted on the ETL conduction band. Of trialled Na⁺, Li⁺ and K⁺ salts, Na⁺ provided the best band-matching, and therefore the greatest device performance. Stacks treated with 3.5 μ l of 0.05 M salt in a GBL/ethanol mix

and annealed at 100 °C before MAPbI₃ infiltration exhibited average PCEs of $15.76 \pm 0.44\%$ compared to control devices of $13.91 \pm 0.38\%$ efficiency [49].

In conventional sandwich devices, solvent-based manipulation of perovskite crystallisation kinetics is also common, where DMF/DMSO mixtures are used to obtain large, high coverage surface grains through increased lead-solvent coordination [107–109]. Unlike the additives mentioned thus far, solvents are removed during annealing and therefore affect performance solely through impacting on crystal formation.

A very limited number of solvent systems are used in mCPSCs, with relatively few examples of solvent additives for targeted infiltration improvement. Most published work on one-step depositions focuses solely on DMF or GBL solvent systems: precursors that prove difficult to infiltrate using the standard one-step formulations are generally applied using two-step methods. Adding ethanol to GBL-based mCPSC precursors was recently shown to improve PCEs from 13.7% to 15.0% and significantly extend the liquid precursor shelf life. This was attributed to increased lead-solvent coordination, resulting in decreased colloidal diameters that concurrently slowed crystallisation and reduced crystal sizes [110]. Some work also exists using *N*-methyl-2-pyrrolidone (NMP), but as this is even more toxic than DMF it is unsuitable for scale-up [111,112].

DMF/DMSO mixtures have also recently been used in conjunction with vapour treatments to obtain high-performance mixed cation mCPSCs with one-step infiltrations (further discussed in Section 3.4). In DMF precursors, formamide and ammonium chloride (NH₂CHO, NH₄Cl) solvent additives have also shown promise in significantly slowing crystallisation during infiltration [57,113]. While the other solvent additives mentioned control crystal size by changing colloidal diameters in the precursor, NH₄Cl instead forms a crystalline NH₄PbI₃ intermediate, which then reacts with ambient moisture to form MAPbI₃. Therefore, NH₄Cl remains in the perovskite post-annealing and must be removed with humidity treatment. When applied in mCPSCs, all device parameters were improved, effectively doubling the average PCE from 6.77% to 13.92%. The cells were also relatively stable, maintaining performance over 130 days in dark storage [113].

In other architectures, improved crystallinity can also be achieved through changing precursor stoichiometry [114–116]. Slight stoichiometric variation can result in films with fewer defect related losses: for example, 5% excess MAI in the precursor was found to improve the V_{oc} of inverted P-I-N devices by 200 mV [117]. In mCPSCs 0.45 M excess MAI in precursor formulations produces hysteresis alleviation, better infiltration and enhanced J_{sc} , with average PCEs improving from 7.09% to 13.35% [118].

In conventional devices, excess PbI₂ is also known to reduce grain boundary defect concentrations, resulting in devices of >20% PCE [115]. However, this is not the case in mCPSCs; AVA-MAPbI₃ devices with excess PbI₂ showed no significant performance enhancement and degraded significantly faster than stoichiometric devices [43]. In mCPSCs, it is possible that the long annealing times cause more MAI loss, resulting a slight excess of PbI₂ at grain boundaries from stoichiometric precursors. A molar excess in the precursor solution could therefore result in a detrimental surplus of PbI₂. Alternatively, AVA alone may be sufficient for surface passivation in mCPSCs, rendering excess PbI₂ obsolete in this case.

3.3. Inorganic Additives

Depending on the ionic radius and valence bonding properties of the relevant ion, inorganic additives sit either at grain boundaries and interfaces or within interstitial gaps in the perovskite lattice. A large variety of metal salts have been incorporated to precursors for conventional devices in recent years, ranging from main group species like Mg²⁺ to transition and rare earth metal salts. A broad range of properties may be influenced in this way, including defect and trap state densities, photoluminescence quantum yields and band positions [58,119–122].

Metal salts with small cations such as KI are known to improve the performance and decrease hysteresis of sandwich devices through grain boundary defect passivation [120,123]. Due to smaller grain sizes, mCPSCs likely have higher defect density per unit active area than

other architectures, and surface passivating salts could therefore be particularly beneficial to performance. However, despite an abundance of work on metal doping in other architectures relatively few examples in mCPSCs have been published at the time of writing. This may be due to the relatively poor solubility of many such salts in GBL, one of the more common solvents for one-step depositions of high-quality infiltration. Currently, the only inorganic additive added to GBL-based systems thus far is CuSCN, a common hole transporter. Although this was not found significantly to affect PCE, its presence reduced hysteresis and improved device response times [83]. This is indicative of improved hole extraction at the carbon electrode, which may result in improved stability under operating conditions.

While DMF solvates a wider variety of salts, it is generally more effective when applied using a two-step process: in the absence of vapour treatments, one step DMF-based infiltrations of mCPSCs tend to form nonuniform films with large needle-like crystals [124]. Consequently, relatively few examples of DMF based one-step depositions with inorganic additives are present in the literature.

Where inorganic additives have been successfully incorporated to one-step depositions, high performance devices often exhibit small, even crystal formations. Although these inorganic additives often produce smaller diameter crystals, the smaller crystallite size is not itself responsible for the observed performance enhancement. The smaller crystal diameters are instead indicative of crystal growth inhibition during the early stages of infiltration, which benefits stack filling through preventing the formation of blockages [125].

For example, the optimum concentrations for mCPSC performance of LiCl and SrCl₂ additives were also those where needle-like crystal formation was completely suppressed [126,127] (Figure 12). A 30% LiCl addition to MAPbI₃ in DMF completely prevents the formation of such structures (Figure 12), resulting in high quality one-step DMF infiltration and ~4% average PCE improvement in mCPSCs. Recombination and film resistivity was also decreased, a consequence of surface defect passivation and Li⁺ doping of the titania interface [126]. Similarly, adding 0.1 molar excess SrCl₂ to mCPSC precursors produces small, densely packed and highly crystalline layers. Concentrated at grain boundaries, this additive acted as an effective defect inhibitor in the annealed film, producing an impressive PCE of 16% [127].

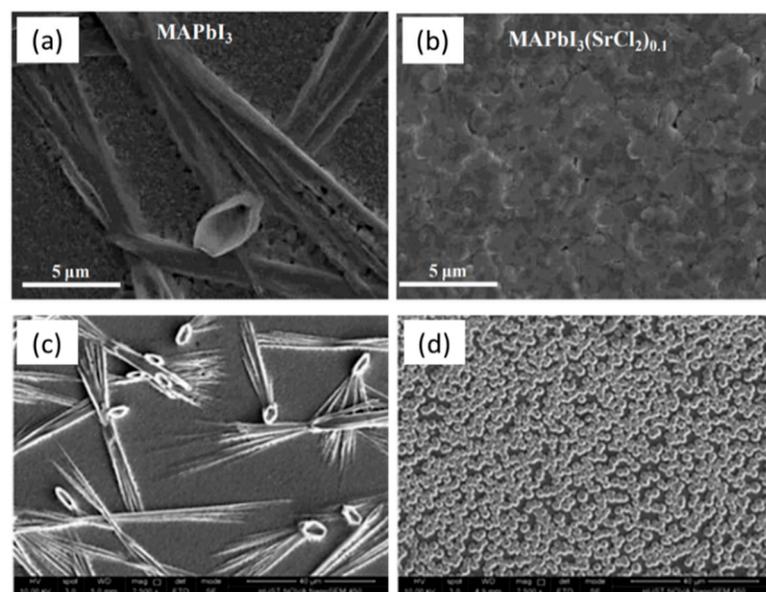


Figure 12. FTO-based films of (a) MAPbI₃ and (b) MAPbI₃.0.3SrCl₂; mesoporous TiO₂ based films of (c) MAPbI₃ and (d) MAPbI₃.0.3LiCl showing suppression of needle-like crystals. (a,b) were adapted with permission from reference [127]. (c,d) were adopted from reference [126] with permission from The Royal Society of Chemistry.

Interestingly, the annealed mCPSCs retained almost all the added Cl^- , unusual as added chloride ions are often difficult to detect post-crystallisation [128]. In these devices, instead of forming volatile MgCl_2 and evaporating during annealing, most of the additive ions incorporated as SrCl_2 on grain surfaces.

A small proportion of the added Sr^{2+} ions also incorporated into the perovskite lattice, as evidenced by shifts in perovskite XRD values. Although no change in the PL emission or optical bandgap was observed, small amounts of Sr^{2+} in the perovskite lattice may account for the improved device stability: increasing the strength of ionic bonds within the unit cell can decrease ion migration. This partial Sr^{2+} incorporation is unexpected as recent work on the mechanism of Sr^{2+} perovskite doping in conventional PSCs found that such high doping levels induce SrCl_2 surface segregation with negligible lattice incorporation [95]. The unexpected partial incorporation of Sr^{2+} into the lattice is possibly due to the vastly different annealing procedures used for mCPSCs. Much work remains to be done on the metal doping of these systems, with scope for applying other dopants as well as unravelling the underlying mechanisms behind additive induced mCPSC performance enhancement.

3.4. Cation Substitution

Any of the perovskite ABX_3 ions may be fully or partially substituted with organic or inorganic species of suitable valence bonding properties and ionic radii, and a huge variety of substitutions are present in the literature [129,130]. As well as influencing optoelectronic properties, ionic substitution can also improve the operational stability of devices: for example, partial substitution of volatile methylammonium with formamidinium (FA) results in advantageous bandgap lowering, while Cs^+ substitution improves optical absorption and system stability [131,132]. Therefore, material characteristics can potentially be customised for optimal performance in different architectures or environments.

In conventional architectures the volatile, oxygen sensitive methylammonium ion significantly curbs operational stability, providing a major driving force for research into alternative cations. This problem is far less apparent in mCPSCs- even without AVA, well-infiltrated MAPbI_3 devices are far more stable under ambient conditions than conventional mesoscopic cells. Such high intrinsic stability may have provided less incentive for work into cation substitution, which could explain the relatively low number of trialled species in mCPSCs thus far. Incorporating substitutions can also be particularly challenging in mCPSCs: small formulation adjustments can negatively impact upon wetting and precursor infiltration. Minor changes in a precursor's ionic properties can also drastically alter crystallisation dynamics, impacting infiltration, device performance and stability. Furthermore, the long, low temperature annealing processes and physical constraint from the mesoporous stack can result in incorrect phase formation and solvent retention. Additional limitations exist for formulations that are poorly soluble in GBL or require higher annealing temperatures for desired phase formation [89,91]. Crystallisation in mCPSCs must therefore be very carefully controlled. However, crystallisation processes within the stack remain relatively poorly understood, making modifications to optimised systems extremely challenging. In these cases, two-step methods offer more control over infiltration and crystal morphology.

One of the first examples of cation substitution in mCPSCs introduced FAPbI_3 perovskites in 2014. Advantageous due to its low bandgap of 1.43 eV and comparatively high thermal stability, α - FAPbI_3 can be difficult to crystallise in mCPSCs as long, low temperature annealing can result in formation of the yellow δ -phase [91,94]. While one step depositions resulted in almost complete δ -phase formation, the two-step method produced the desired α -phase and an mCPSC of 11.4% PCE (Figure 13). However, a gradual phase change to the inactive yellow phase was still observed, decreasing device performance over time under operating conditions [58].

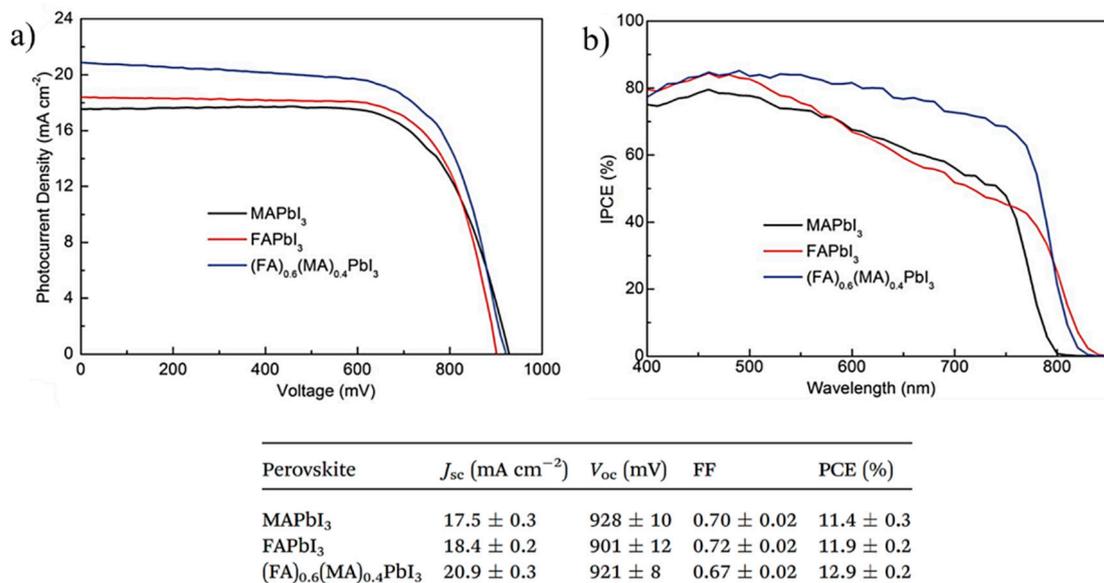


Figure 13. (a) J–V curves of mesoscopic perovskite solar cells with MAPbI₃, FAPbI₃ and (FA)_{0.6}(MA)_{0.4}PbI₃ under 100 mWcm^{−2} AM 1.5G illumination; (b) corresponding IPCE spectra; Table of photoelectric parameters of mesoscopic solar cells with MAPbI₃, FAPbI₃ and (FA)_{0.6}(MA)_{0.4}PbI₃ under 100 mWcm^{−2} AM 1.5G illumination. Adopted from reference [94] with permission from The Royal Society of Chemistry.

The phase instability of FAPbI₃ is a consequence of the large FA⁺ ionic radius and can be mitigated through inclusion of a smaller cation such as MA⁺. These mixed systems have lower bandgaps and improved thermal stability compared to MAPbI₃ and are more resistant to δ -FAPbI₃ formation. Mixed films also exhibit higher optical absorption in the 600–800 nm range than either single cation system. Separate studies have found that MA_{0.6}FA_{0.4}PbI₃ formulations produce the best mCPSCs, with PCE improvements of over 1% compared to MA⁺ and FA⁺ only systems. This is also true of conventional architectures [58,94,133].

The performance and stability of mCPSCs can be further enhanced through Cs⁺ addition. MA⁺ and Cs⁺ improve performance in distinctly different ways: while MA⁺ increases light absorption and hence the number of generated carriers, Cs⁺ reduces recombination and thus improves carrier collection. When added to MA_{0.6}FA_{0.4}PbI₃ mCPSCs, Cs⁺ improved V_{oc} and J_{sc} by effectively doubling the carrier diffusion length. In the optimised system, electron and hole carrier diffusion lengths went from 70 nm and 1.9 μm to 140 nm and 0.95 μm for MA_{0.6}FA_{0.4}PbI₃ and Cs_{0.05}(MA_{0.6}FA_{0.4})_{0.95}PbI₃ systems respectively [58].

Whilst decreasing MA⁺ content through partial substitution can improve perovskite stability, the most resilient materials exclude it altogether. Cs⁺ can be used to stabilise α -FAPbI₃ in the absence of MA⁺, leading to extremely thermally stable mCPSCs. For example, a 2019 paper found that 10% Cs incorporation into the lead solution for the first step of two step deposition effectively halved mCPSC performance loss under thermal stress. Cs_{0.1}FA_{0.9}PbI₃ mCPSCs kept at 85 °C at 25% RH retained 70% of initial PCE, compared to 35% for FA-only cells. Cell performance was also enhanced, with Cs_{0.1}FA_{0.9}PbI₃ producing mCPSCs of >14% PCE compared to 10.8% for FAPbI₃ [89].

One-step Cs_{0.1}FA_{0.9}PbI₃ infiltration of mCPSCs has also recently been achieved with a vapour-controlled crystallisation technique. In this method, DMSO was introduced to the precursor and annealing carried out in a solvent vapour environment to slow crystallisation: highly coordinating DMSO reduces precursor colloidal diameters and a vapour environment slows solvent removal. Produced devices exhibited significant J_{sc} and FF improvements over controls, resulting in PCEs of 15% compared to 10% for MAPbI₃. Devices were also found to be stable over 300 h of dark storage at 50% RH- although it should be noted that such devices have been found in some studies to undergo transitions

to the δ -phase over time, with additional mixed halide stabilisation required for long term stability under light [89,90].

This vapour controlled crystallisation was also recently used to incorporate rubidium iodide into mCPSCs, using DMF/DMSO-based $\text{Cs}_{0.1}\text{Rb}_{0.05}\text{FA}_{0.85}\text{PbI}_3$ precursors. Rubidium has been found to improve performance and alleviate hysteresis in conventional devices through increasing charge carrier mobility. Too small to sit within A lattice sites, it incorporates as a performance enhancing additive concentrates at surface defects and in interstitial sites. This results in reduced trap assisted recombination and extends the operational stability of devices by limiting ion migration in the perovskite. Combined with the stabilising effect and V_{oc} enhancement from Cs^+ this produced an mCPSC with a maximum stabilised PCE of 16.26%, one of the highest so far [57]. While two-step and vapour-assisted methods produce high-performance devices, they are not particularly suited to scale-up: two-step methods require extra time and materials, while vapour treatments require a highly controlled annealing environment. Ideally, a one-step method that does not require additional process control or post treatment is needed to be attractive for application to larger scale modules: hence the continued use of AVA/GBL systems in scale-up projects [25,60]. To the best of our knowledge only one example of an mCPSC combining Cs^+ substitution with AVA addition exists thus far. Including inorganic cations in AVA-MAPbI₃ systems is particularly challenging due to the poor solubility of salts like CsI in GBL. In this example, Cs^+ and AVA were incorporated together as caesium-aminovaleric acetate (CsAVA) and added as a 5% molar ratio to produce GBL-based $\text{Cs}_{0.05}\text{MA}_{0.95}\text{Pb}(\text{AVA})_{0.05}\text{I}_{2.95}$. Devices exhibited higher J_{sc} and V_{oc} as well as improved thermal stability over 500 h in an inert environment. Combining the superior infiltration and photostability of AVA and the optoelectronic tunability of mixed cation systems could be an avenue into achieving extremely stable mCPSCs of high PCE and merits further work [134].

3.5. Anion Substitution

Changing the X ion directly affects the valence and conduction band energies of the perovskite absorber as the highest occupied molecular orbitals (HOMOs) and lowest unoccupied molecular orbitals (LUMOs) consist of Pb-X antibonding orbitals [135]. Like with cation substitution, differences in the ionic radii of substituted ions also cause changes to unit cell size and shape, affecting properties such as charge transfer and phase stability [136].

The X^- anion is most commonly a halide or combination thereof, although other anions of similar size such as SCN^- have also been explored in conventional devices [137]. Iodide systems are most common due to a broad absorption range and relatively small bandgap (~1.5 eV). However, they are also less stable towards water, oxygen and heat [130,138,139].

MAPbBr₃ is photoactive and thermally stable, and partial substitution with bromide and other anions is common in conventional architectures for stability enhancement, bandgap tuning and increased V_{oc} [130]. However, these V_{oc} and stability gains are often accompanied by lower absorption efficiency, which can lead to lower overall PCE. For example, in mCPSCs MAPbBr₃ produces high V_{oc} : up to 1.33 V, while typical MAPbI₃ mCPSCs are usually ~0.9 V. However, current densities of under 7 mAcm^{-2} resulted in a comparatively low PCE of 7.11%, compared to 12% for MAPbI₃ [140]. Bromide ratios must therefore be carefully optimised to avoid efficiency depleting J_{sc} losses (Figure 14). For example, depositing MAPbI₂Br to mCPSCs using a two-step method resulted in open circuit voltages of over 1 V and 11% PCE compared to 0.9 V and 10.5% for MAPbI₃. In this case, the substantial V_{oc} improvement outweighed J_{sc} losses from the bandgap increase. Average mCPSC PCEs of 12.76% can be obtained with optimised bromide substitutions of 10% [141] (Figure 14). In both cases, the mCPSCs produced were also more stable to photodegradation under operating conditions [142].

Bromide substitutions have also been found to improve the performance and further stabilise mixed cation mCPSC. For example, mCPSCs incorporating performance optimised $\text{Cs}_{0.1}\text{FA}_{0.9}\text{PbI}_{0.9}\text{Br}_{0.1}$ exhibited no δ -phase formation, improved thermal stability and higher PCEs than iodide only formulations [58,89]. Interestingly, the performance enhancements of

Br^- and Cs^+ substitution were found to be additive, with mCPSCs using combined systems exhibiting higher optical absorption, longer carrier lifetimes and higher performance than either substituent in isolation (Figure 15). At such low quantities (10% of halide composition), Br^- had no negative effect on mCPSC absorption or J_{sc} , although it should be noted that this amount was not sufficient to fully stabilise the α -phase without additional Cs^+ [89].

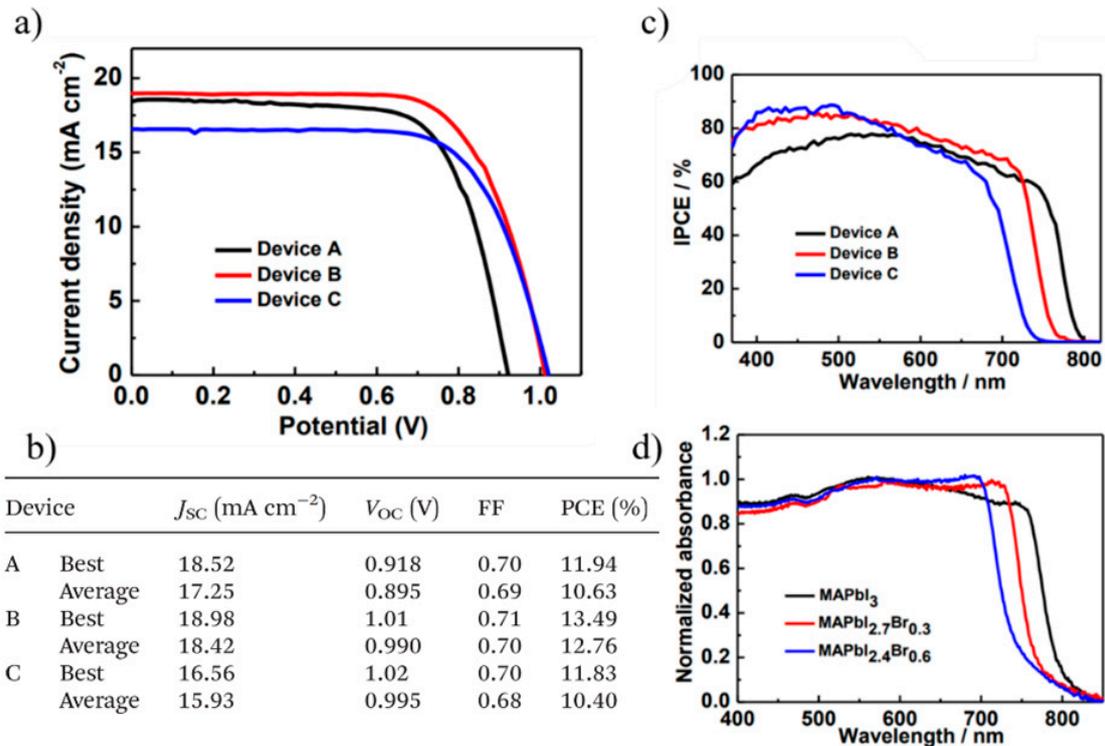


Figure 14. (a) Current density-voltage (J-V) curves for devices A (MAPbI₃) (black), device B (MAPbI_{2.7}Br_{0.3}) (red), and device C (MAPbI_{2.4}Br_{0.6}) (blue) under 1 sun illumination (100 mW cm⁻²); (b) Photovoltaic performance of devices A-C under AM 1.5G illumination; (c) the corresponding IPCE for the devices; (d) UV-Vis absorption spectra. Adapted from reference [141].

Apart from performance decreases due to lower absorption, one of the main issues with these materials is light-induced segregation, where iodine rich areas form with constant illumination. These areas form traps that increase the rate of recombination, negatively affecting performance under operative conditions [143]. As MAPbI₃ is more stable in mCPSCs than other architectures there is less incentive to improve stability to the detriment of optical absorption and operational performance. This may have somewhat limited work on mixed anion mCPSCs.

Halide alternatives include large anions of lower charge density such as BF_4^- or SCN^- , both of which have been shown to improve thermal stability and moisture tolerance of perovskite layers [144,145]. The BF_4^- has been used to produce planar devices of over 20% PCE, with extremely high carrier lifetimes and low defect density [146]. In MAPbI₃ mCPSCs prepared by one-step DMF depositions a 5% BF_4^- substitution increased PCE from 9.65% to 12.24%, with significant improvements to all device parameters [147]. A similar level of performance improvement is seen in one-step GBL depositions, which achieved a champion PCE of 15.5% compared to the 13.4% AVA-MAPbI₃ champion control [148]. These ‘pseudo-halide’ substituents can substantially improve performance and lack the segregation issues observed in mixed halide perovskites and could therefore offer an alternative route to anion substitution in mCPSCs. However, other alternatives such as SCN^- have yet to be used in mCPSCs.

The methods, materials perovskite formulations and materials discussed in this method are presented in Table 2.

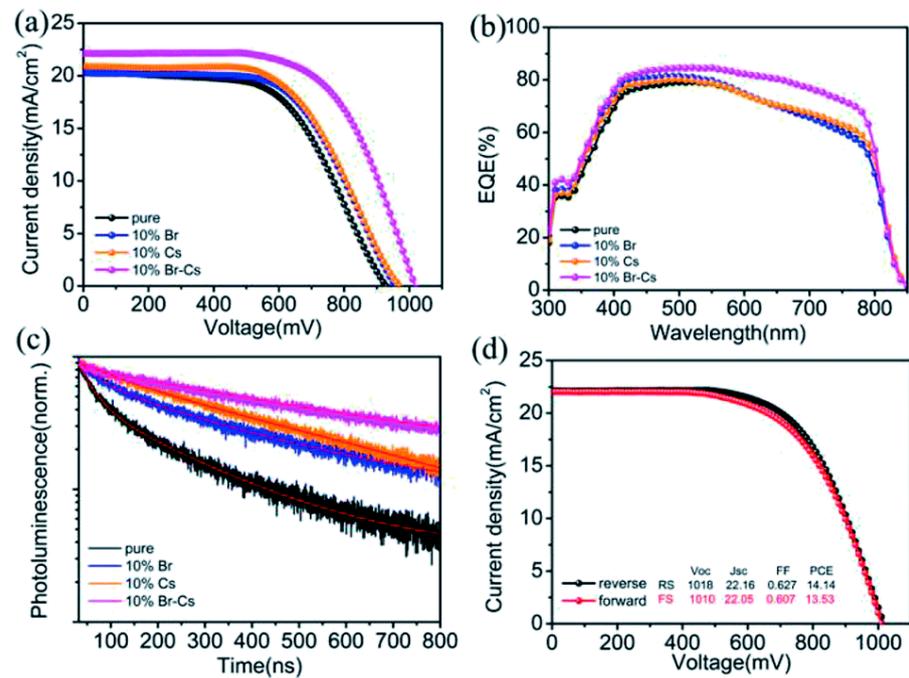


Figure 15. (a) Photocurrent density–voltage (J–V) curves of different FAPbI₃-based PSCs; (b) the corresponding EQE; (c) TRPL spectra of different FAPbI₃-based perovskite films; (d) J–V curves of Cs_{0.1}FA_{0.9}PbBr_{0.1}I_{2.9}-device under forward and reverse scan. [89]—Published by The Royal Society of Chemistry.

Table 2. Summary of reported mCPSCs, with information about materials, processing and PCE values. All the devices were prepared on FTO glass. Deposition methods are mentioned with the following notes: ⁽¹⁾ spray pyrolysis, ⁽²⁾ screen-printing, ⁽³⁾ spin coating, ⁽⁴⁾ chemical bath deposition ⁽⁵⁾ doctor blading. Infiltration methods are mentioned with the following notes: (a) drop casting. The best performing devices from each article are listed here. When a range of thickness is reported, the table refers to an average value. where two references are reported, the associated works include the first example of a given perovskite in mCPSCs and the publication presenting the current highest PCE for that formulation.

Ref	Inorganic Stack Materials, Deposition Method and Thickness	Perovskite Formulation	PCE (%) Illumination Area (mm ²), Publication Year	V _{oc} (mV) J _{sc} (mA/cm ²) FF (%)	Comments
[147]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽²⁾ (0.4 μm)/ ZrO ₂ ⁽²⁾ (1.5 μm)/C ⁽²⁾ (10 μm)	MAPbI ₃ , one-step ^(a) , DMF	10.5(7) 2016	914, 16.92, 68	
[127]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽²⁾ (0.4 μm)/ Al ₂ O ₃ ⁽²⁾ (0.5 μm)/C ⁽²⁾ (8 μm)	MAPbI ₃ , one-step ^(a) , DMF	13(16) 2017	950, 19.5, 70	
[94,148]	TiO ₂ ⁽¹⁾ (0.1 μm)/TiO ₂ ⁽²⁾ (0.7 μm)/ZrO ₂ ⁽²⁾ (1.5 μm)/ C ⁽²⁾ (10 μm)	MAPbI ₃ , two-step, DMF, IPA	11.4(7) 2014	928, 17.5, 70	Average cell values
[111]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽²⁾ (1 μm)/ Al ₂ O ₃ ⁽²⁾ (1 μm)/C ⁽²⁾ (10 μm)	MAPbI ₃ , one-step ^(a) , NMP	15(9) 2017	893, 22.43, 75	Highly controlled solvent vapour environment
[14]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽²⁾ (1 μm)/ ZrO ₂ ⁽²⁾ -(1 μm)/C ⁽²⁾ (10 μm)	MAPbI ₃ , one-step ^(a) , GBL	6.6(12.5) 2013	878, 12.4, 61	With Spheroidal graphite
[38,83]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽²⁾ -(0.5 μm)/ ZrO ₂ ⁽²⁾ (1.5 μm)/C ⁽²⁾ (20 μm)	AVA _x MA _(1-x) PbI ₃ , one-step ^(a) , GBL	15.3(16) 2019	869, 21.2, 83	30 min ambient infiltration time, 5 day humidity treatment

Table 2. Conts.

Ref	Inorganic Stack Materials, Deposition Method and Thickness	Perovskite Formulation	PCE (%) Illumination Area (mm ²), Publication Year	V _{oc} (mV) J _{sc} (mA/cm ²) FF (%)	Comments
[110]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽²⁾ (0.5 μm)/ ZrO ₂ ⁽²⁾ (3 μm)/C ⁽²⁾ (10 μms)	AVA _x MA _(1-x) PbI ₃ , one-step ^(a) , GBL/Ethanol	15.1(10) 2019	900, 23.58, 71	
[99]	TiO ₂ ⁽³⁾ /TiO ₂ ⁽³⁾ (0.5 μm)/ ZrO ₂ ⁽³⁾ (1.5 μm)/C ⁽³⁾ (13 μm)	AVA _x MA _(1-x) PbI ₃ , one-step, DMF	9.1 2020	848, 23.8, 45	
[101]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽³⁾ (2 μm)/ ZrO ₂ ⁽²⁾ (1.8 μm)/C ⁽⁵⁾ (24 μm)	AVA _x MA _(1-x) PbI ₃ .PEAI one-step	6.72 2020	875, 15.8, 49	Extremely thick TiO ₂
[83]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽²⁾ (1 μm)/ ZrO ₂ ⁽²⁾ (2.2 μm)/C ⁽²⁾ (20 μm)	AVA _x MA _(1-x) PbI ₃ , 0.04 CuSCN, one-step, GBL	15(16) 2019	888, 21.9, 77	30 min ambient infiltration time, 5 day humidity treatment
[99]	TiO ₂ ⁽⁴⁾ /TiO ₂ ⁽³⁾ (0.5 μm)/ ZrO ₂ ⁽³⁾ (1.5 μm)/C ⁽³⁾ (13 μm)	(Leucine) _x MA _(1-x) PbI ₃ , one-step ^(a) , DMF	9.0 2020	838, 24.2, 45	
[98]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽²⁾ (0.5 μm)/ ZrO ₂ ⁽²⁾ (2 μm)/C ⁽²⁾ (15 μm)	AB _x MA _(1-x) PbI ₃ , one-step ^(a) , GBL	15.6(12.6) 2018	940, 23.4, 71	
[149]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽²⁾ (0.5 μm)/ ZrO ₂ ⁽²⁾ (2 μm)/C ⁽²⁾ (15 μm)	MAPbI ₃ , 2% MLAI, one-step, DMF/DMSO 4:1	13.86(10) 2020	940, 20.34, 72	
[100]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽²⁾ /ZrO ₂ ⁽²⁾ /C ⁽²⁾	MAPbI ₃ .0.15CH ₄ N ₂ S, one-step ^(a) , DMF	13.0 2019	910, 22.39, 64	
[148]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽²⁾ (0.6 μm)/ ZrO ₂ ⁽²⁾ (2.5 μm)/C ⁽²⁾ (12 μm)	AVA _{0.034} MA _{0.9} PbI _{2.95} (BF ₄) _{0.05} one-step ^(a) , GBL	15.5(10) 2018	970, 24.37, 66	
[104,147]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽²⁾ (0.4 μm)/ ZrO ₂ ⁽²⁾ (1.5 μm)/C ⁽²⁾ (10 μm)	MAPbI _{2.95} (BF ₄) _{0.05} , one-step ^(a) , DMF	13.24(7) 2016	957, 18.15, 76	
[134]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽²⁾ (0.5 μm)/ ZrO ₂ ⁽²⁾ (1.4 μm)/C ⁽²⁾ (10 μm)	CsAVA _{0.05} MA _{0.95} PbI ₃ , one-step, GBL	12.2(14.8) 2018	893, 20.59, 66	High annealing temp. (100 °C, 1 h)
[118]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽²⁾ (1 μm)/ ZrO ₂ ⁽²⁾ (1.6 μm)/C ⁽²⁾ (11 μm)	MAPbI ₃ .0.45MAcI, one-step, DMF	14.5(14.8) 2018	1033, 20.14, 70	High annealing temp. (100 °C, 20 min)
[102]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽²⁾ (0.5 μm)/ ZrO ₂ ⁽²⁾ (0.5 μm)/C ⁽²⁾ (10 μm)	MAPbI ₃ .0.25GuCl, one-step ^(a) , DMF	14.35(12.6) 2017	1000, 19.31, 74	
[105]	TiO ₂ ⁽³⁾ /TiO ₂ ⁽²⁾ (1.4 μm)/ ZrO ₂ ⁽²⁾ (1.4 μm)/C ⁽²⁾	MAPbI ₃ .0.1CQD, one-step ^(a) , DMF	7.62 2018	790, 16.4, 62	With TiO ₂ nanosheets
[104]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽²⁾ (0.6 μm)/ ZrO ₂ ⁽²⁾ (2 μm)/C ⁽²⁾ (12 μm)	MAPbI ₃ , 25mg/mL PCBM one-step ^(a) , DMF	12.36 2018	930, 20.26, 66	High annealing temp. (100 °C, 5 min)
[104]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽²⁾ (0.6 μm)/ ZrO ₂ ⁽²⁾ (2 μm)/C ⁽²⁾ (12 μm)	MAPbI _{2.95} (BF ₄) _{0.05} , 25mg/mL PCBM, one-step ^(a) , DMF	14.26 2018	990, 20.55, 70	High annealing temp., (100 °C, 5 min)
[113]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽²⁾ (1 μm)/ ZrO ₂ ⁽²⁾ (1 μm)/C ⁽²⁾ (10 μm)	MAPbI ₃ , 53.3mg/mL NH ₄ Cl, one-step, DMF	15.6(12.6) 2017	940, 21.45, 77	With humidity exposure
[126]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽²⁾ (0.5 μm)/ ZrO ₂ ⁽²⁾ (2 μm)/C ⁽²⁾ (10 μm)	MAPbI ₃ .0.3LiCl, one-step ^(a) , DMF	14.5(10) 2016	927, 20.2, 77	High annealing temp. (100 °C, 10 min)
[127]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽²⁾ (0.4 μm)/ Al ₂ O ₃ ⁽²⁾ (0.5 μm)/C ⁽²⁾ (8 μm)	MAPbI ₃ .0.1SrCl ₂ , one-step ^(a) , DMF	15.9(16) 2017	1050, 20.15, 75	

Table 2. *Conts.*

Ref	Inorganic Stack Materials, Deposition Method and Thickness	Perovskite Formulation	PCE (%) Illumination Area (mm ²), Publication Year	V _{oc} (mV) J _{sc} (mA/cm ²) FF (%)	Comments
[94]	TiO ₂ ⁽¹⁾ (0.1 μm)/TiO ₂ ⁽²⁾ (0.7 μm)/ZrO ₂ ⁽²⁾ (1.5 μm)/C ⁽²⁾ (10 μm)	FAPbI ₃ , two-Step, DMF, IPA	11.9(7) 2014	901, 18.4, 72	
[94]	TiO ₂ ⁽¹⁾ (0.1 μm)/TiO ₂ ⁽²⁾ (0.7 μm)/ZrO ₂ ⁽²⁾ (1.5 μm)/C ⁽²⁾ (10 μm)	FA _{0.4} MA _{0.6} PbI ₃ , two-step, DMF, IPA	12.9(7) 2014	921, 10.9, 67	
[58]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽²⁾ (0.46 μm)/ Al ₂ O ₃ ⁽²⁾ (0.45 μm)/ NiO ⁽²⁾ (0.8 μm)/C ⁽²⁾ (10 μm)	FA _{0.4} MA _{0.6} PbI _{2.8} Br _{0.2}	14.88(12.9) 2017	953, 22.31, 70	NiO hole extraction layer present
[58]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽²⁾ (0.46 μm)/ Al ₂ O ₃ ⁽²⁾ (0.45 μm)/ NiO ⁽²⁾ (0.8 μm)/C ⁽²⁾ (10 μm)	Cs _{0.05} (FA _{0.4} MA _{0.6}) _{0.95} PbI _{2.8} Br _{0.2} , two-step, DMF, IPA	17(12.9) 2017	1008, 23.4, 72	NiO hole extraction layer present
[90]	TiO ₂ ⁽¹⁾ (0.1 μm)/TiO ₂ ⁽²⁾ (1 μm)/ ZrO ₂ ⁽²⁾ (2 μm)/C ⁽²⁾ (10 μm)	Cs _{0.1} FA _{0.9} PbI ₃ , one-step, DMF/DMSO	15(10) 2019	920, 23.63, 69	Solvent vapour assisted crystallisation.
[89]	TiO ₂ ⁽¹⁾ (0.1 μm)/TiO ₂ ⁽²⁾ (1 μm)/ ZrO ₂ ⁽²⁾ (1.5 μm)/C ⁽²⁾ (10 μm)	Cs _{0.1} FA _{0.9} PbI ₃ , two-step, DMF, IPA	11.72(14.8) 2019	970, 20.91, 58	
[89]	TiO ₂ ⁽¹⁾ (0.1 μm)/TiO ₂ ⁽²⁾ (1 μm)/ ZrO ₂ ⁽²⁾ (1.5 μm)/C ⁽²⁾ (10 μm)	FAPbI _{2.9} Br _{0.1} , two-step, DMF, IPA	11.53(14.8) 2019	950, 20.29, 59	
[89]	TiO ₂ ⁽¹⁾ (0.1 μm)/TiO ₂ ⁽²⁾ (1 μm)/ ZrO ₂ ⁽²⁾ (1.5 μm)/C ⁽²⁾ (10 μm)	Cs _{0.1} FA _{0.9} PbI _{0.9} Br _{0.1} , two-step, DMF, IPA	14.14(14.8) 2019	1018, 22.16, 63	
[57]	TiO ₂ ⁽¹⁾ (0.1 μm)/TiO ₂ ⁽²⁾ (0.5 μm)/ ZrO ₂ ⁽²⁾ (0.75 μm)/C ⁽²⁾ (15 μm)	Cs _{0.1} Rb _{0.05} FA _{0.85} PbI ₃ , one-step, DMF/DMSO/5% Formamide	16.21(10.8) 2019	909, 22.69, 79	2 h solvent vapour assisted crystallisation.
[57]	TiO ₂ ⁽¹⁾ (0.1 μm)/TiO ₂ ⁽²⁾ (0.5 μm)/ ZrO ₂ ⁽²⁾ (0.75 μm)/C ⁽²⁾ (15 μm)	Cs _{0.1} Rb _{0.05} FA _{0.85} PbI ₃ , one step, DMF/DMSO	14.59(10.8) 2019	888, 22.56, 72	2 h solvent vapour assisted crystallisation Average device values.
[57]	TiO ₂ ⁽¹⁾ (0.1 μm)/TiO ₂ ⁽²⁾ (0.5 μm)/ ZrO ₂ ⁽²⁾ (0.75 μm)/C ⁽²⁾ (15 μm)	Cs _{0.1} Rb _{0.05} FA _{0.85} PbI ₃ , one-step, DMF	6.42(10.8) 2019	867, 11.12, 66	2 h solvent vapour assisted crystallisation. Average device values.
[141]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽²⁾ (0.4 μm)/ Al ₂ O ₃ ⁽²⁾ (1.2 μm)/C ⁽²⁾ (10 μm)	MAPbI _{2.7} Br _{0.03} , two-step, DMF, IPA	13.49 2016	1010, 18.93, 71	
[142]	TiO ₂ ⁽¹⁾ /TiO ₂ ⁽²⁾ (0.4 μm)/ Al ₂ O ₃ ⁽²⁾ (0.6 μm)/C ⁽²⁾ (10 μm)	MAPbI ₂ Br, two-step, DMF, IPA	11.03(16) 2015	1040, 15.37, 69	

4. Stability Studies

One of the main bottlenecks to PSC commercialisation is the poor long-term stability of devices. The mCPSC architecture is perhaps the most promising PSC architecture for highly stable modules as the absence of unstable HTLs performance damaging noble metals by hydrophobic carbon electrodes prevent ambient moisture penetrating the perovskite layer and therefore improving device stability [150]. The first mCPSC, fabricated in 2013

with a PCE of 6.64% proved stable for over 840 h of dark storage [14]. A great deal of work has since focused on further enhancing this impressive stability.

As discussed in Section 3, AVA-MAPbI₃ represented a huge breakthrough, with devices achieving a certified PCE of 12.8% compared to 7.2% for MAPbI₃. Moreover, the unsealed AVA-MAPbI₃ mCPSC was stable for over 1008 h in ambient air under full sunlight, an unprecedented result for any PSC architecture. The same group also produced a larger area module of 10 serially connected cells (10 × 10 cm², active area 49 cm²), which recorded a 10.4% PCE and a light-soaked stability of 1000 h [30]. Outdoor stability of encapsulated devices has also been tested, by exposing the device in the local environment in Wuhan, China for 1 month. Finally, the unsealed modules were stored in the dark for over 1 year. No significant degradation was observed under any of these conditions, indicating good operational and environmental stability and providing the incentive for continued scale-up development. The largest module to undergo significant long term stability testing 70 cm² active area mCPSC module with high efficiency and stability has been reported by Priyadarshi et al. [151]. This fully printed module recorded 10.74% PCE and lost less than 5% initial PCE over more than 2000 h in ambient conditions.

The outdoor durability of encapsulated mCPSCs has also been tested in hot, dry desert climates (Jeddah, Saudi Arabia) [26]. The week-long test was supported with some indoor studies, where devices were subjected to prolonged heat stress (80–85 °C, in closed oven, 90 days) or continuous light-soaking (40 days). There was no evidence of device degradation over these tests [26]. It should be noted that in this instance the devices were encapsulated for the heat test and held in Ar while for illumination experiments.

In 2017, Grancini et al. studied the effect of mixing the (AVAI:PbI₂) and (MAI:PbI₂) precursors at different molar ratios, and found that the addition of 3% AVAI facilitated 2D/3D phase formation at the perovskite-TiO₂ interface [27]. As discussed in Section 3, this mixed phase formation combines the enhanced stability of 2D perovskite with the panchromatic absorption and excellent charge transport of the 3D structure, enabling the fabrication of efficient and stable solar cells. Here, a 100 cm² mCPSC module (active area 46.7 cm²) recorded 11.2% PCE and long-term stability of >10,000 h under continuous illumination at 1000 W m⁻² with temperature cycling maximum reaching 90 °C (Figure 16). The devices were sealed with a glass cover under ambient atmosphere and an ultraviolet filter up to 390 nm was on top of all over the samples.

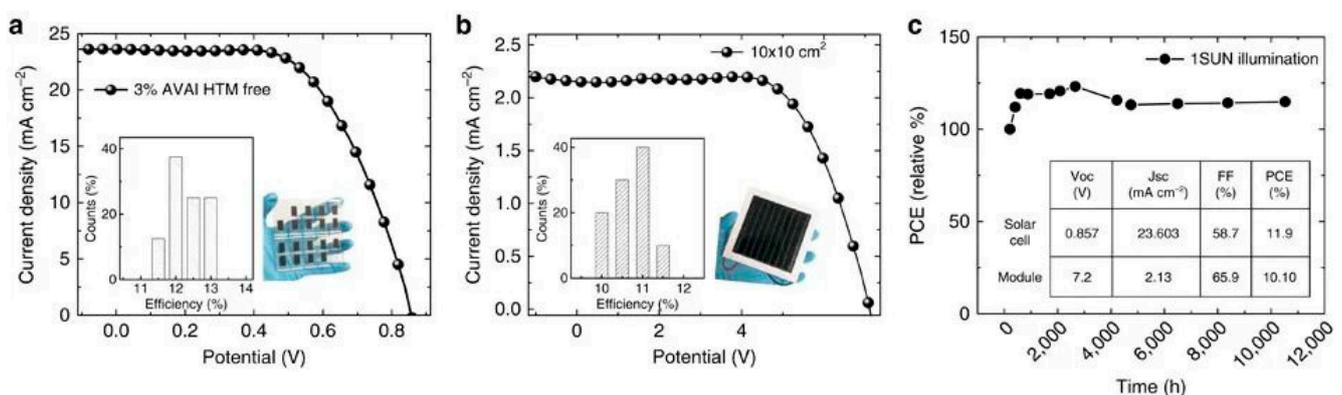


Figure 16. (a) J–V curve using the 2D/3D perovskite with 3% AVAI in mCPSC measured under illumination (device statistics and picture in the inset). (b) J–V curve of a mCPSC 10 × 10 cm² module (device statistics and picture in the inset). (c) Typical module stability test under 1 sun AM 1.5 G conditions. Reproduced from reference [27].

In 2020, polyurethane (PU) and glass encapsulated AVA-MAPbI₃ mCPSCs passed >9000 h IEC61215:2016 standard maximum power point testing without obvious degradation (Figure 17) [28,152]. This study probed the reasons for device degradation. The main failure mechanisms of MAPbI₃ devices were found to be MAI escape at grain boundaries or crystal structure rearrangements for larger and smaller crystals respectively. As well as the Irre-

versible long-distance ionic migration under light, heat, and electrical bias was also found to be highly detrimental. The presence of AVA at the grain boundaries decreased grain sizes and strengthened the crystal boundaries, inhibiting decomposition and crystal rearrangements. Consequently, the ionic migration became reversible, and the device more stable.

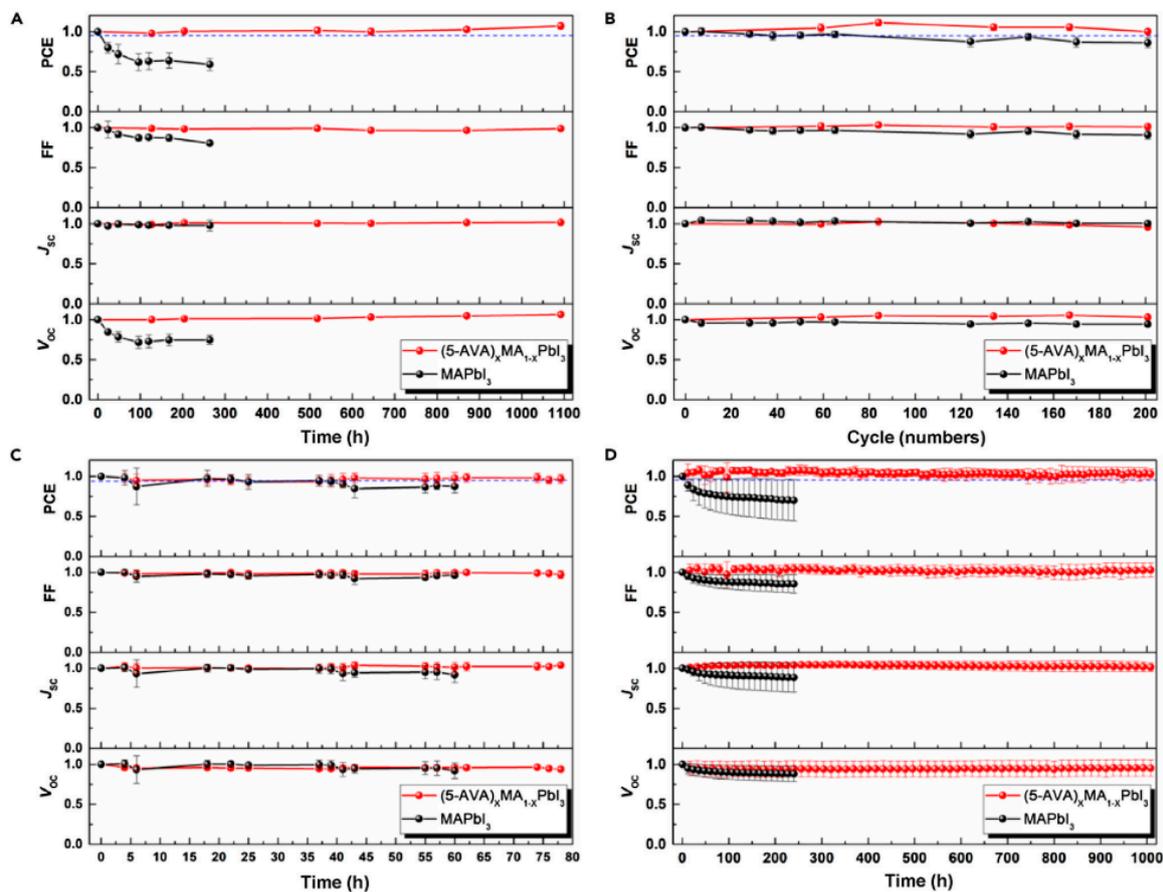


Figure 17. Statistical stability results of IEC61215:2016 and maximal power point tracking (MPPT) Tests for mCPSC containing MAPI with and without AVA. Performance evolutions of devices under (A) the damp heat test (85 °C/85% RH, 1100 h), (B) thermal cycling test (−40 °C–85 °C, 200 cycles), (C) ultraviolet preconditioning test (60 °C, 50 kWh m^{−2}), and (D) MPPT light soaking test (55 °C, 1000 h). The dashed lines correspond to 95% of the initial PCE. The error bars represent the standard deviation. Reproduced with permission from reference [28].

Stability can also be enhanced through applying post-annealing treatments to the perovskite to improve crystallinity. For example, NH₄Cl and moisture act synergistically on perovskite in the TiO₂/ZrO₂/Carbon scaffold (Section 3) [113]: Annealed devices exposed to ambient air showed dramatic efficiency improvements in the first 10–48 h. The resultant device stability was dependent on the ambient humidity. Devices kept in 35% relative humidity (RH) remained stable at high PCE, whereas stability decreased slightly for 45% and 55% RH and decayed sharply for 65% RH. The performance and stability enhancements were directly in line with improving the quality of the crystalline absorber, proving that crystal quality impacts on stability. The devices fabricated under ambient 35% RH showed a PCE of 15.6%, and ambient lifetime of over 130 days, with performance maintaining ~96.7% of the initial value.

Replacing the MA⁺ cation with more thermally stable (NH₂)CH⁺ (FA) or the Cs⁺ containing perovskites is a common strategy for enhancing mCPSC thermal stability (Section 3). For example, double cation mCPSCs with Cs_{0.1}FA_{0.9}PbBr_{0.1}I_{2.9} perovskite maintained 90% of initial PCE after 500 h at a constant temperature of 85 °C, while the device with pristine FAPbI₃ perovskite lost >60% over the same period. To further evaluate

the long-term stability of the device under thermal stress, thermal cycling tests between 85 °C and 30 °C were also performed. After 100 cycles, the $\text{Cs}_{0.1}\text{FA}_{0.9}\text{PbBr}_{0.1}\text{I}_{2.9}$ devices showed no obvious decay in PCE [89].

As well as optimising the device components, good encapsulation is critical for achieving long-term PSC stability. Thus, some works have instead focused on optimising mCPSC encapsulation. For example, Fu et al. studied the use of three different types of hot melt films (polyurethane, PU; polyolefin, POE; and ethylene vinyl acetate, EVA) together with glass sheets for encapsulating mCPSCs. PU significantly enhanced the thermal stability of the device at 85 and 120 °C. The PU/glass system was then used to encapsulate a 100 cm² printable mCPSC mini-module, which proceeded to maintain 97.52% of the initial PCE after 2136 h under outdoor conditions [152].

An advanced laser-assisted glass-frit encapsulation method was also developed to hermetically seal HTM-free PSCs. The PCE of the hermetically encapsulated devices remained constant for 500 h under humid air feeding exposure ($80 \pm 5\%$ RH) while non-hermetically encapsulated devices degraded after 50 h [153]. In further tests, sealed devices passed 70 thermal cycles (−40 °C to 85 °C) and 50 h damp heat (85 °C, 85% RH) tests according to IEC61646 standards.

Reporting device stability is becoming as important as presenting PV parameters. However, despite the great emphasis placed on PSC stability testing there is a distinct lack of consistency in experimental procedure and the way parameters are reported. To facilitate comparisons between stability data from different laboratories device architectures in order to improve reproducibility and better understand failure modes, researchers from many different laboratories recently published a consensus on PSC testing procedures.

The suggested protocols build on the pre-existing International Summit on Organic Photovoltaic Stability (ISOS) parameters for organic photovoltaics (OPV), which have proven instrumental in uncovering various degradation pathways in OPV devices [154]. Based on this work, an inter-laboratory study on the stability of mCPSCs was recently conducted [155]. In this study, AVA-MAPbI₃ mCPSCs ranging from 4.5% to 6% PCE were encapsulated and sent to different laboratories across Europe for testing. Devices under continuous illumination at open circuit conditions survived for only a few hours, but recovered after being stored in the dark, while maximum power point tracking and LED illumination resulted in slower degradation. Interestingly, outdoor tests in two different locations (Barcelona, Spain and Paola, Malta) showed minor degradation over 30 days, demonstrating that natural day/night cycling under real-world conditions could be beneficial for long-term mCPSC operation- in other words, devices may last longer in real world cycling conditions than in laboratory tests. Outdoor testing may therefore offer a more realistic and representative approach to assessing mCPSC stability.

It should be noted that higher PCE was recorded in the early morning and late evening, with a significant drop was observed at around midday, as the severe iR-drop (current-resistance) in the mCPSC makes them prefer lower illumination conditions. This was also observed in indoor conditions, potentially making mCPSCs most suitable for indoor and 'Internet of things' applications [51].

Although many groups have reported remarkable long-term stability measurements, stable, high performance mCPSCs can be hard to produce reproducibly: the multiple interfaces in the mesoporous structure often result in more variation in performance and stability between devices. Improving current understanding of how the internal interfaces and components impact ageing and performance would likely offer greater insight into how to develop of new strategies for improving performance consistency and device stability.

Even though mCPSCs are currently less well-understood and exhibit slightly lower performance than other architectures, the stability far outpaces that of other architectures, with a record >10,000 h lifetime under continuous illumination and a very recent presented device with over 9000 h stability at continuous maximum power condition at 55 °C \pm 5 °C without decay [156]. Table 3 shows the results of the most stable PSC devices, with mCPSCs clearly representing outperforming other architectures over long periods in harsh

conditions [27,28]. It is this impressive stability that makes these devices attractive for commercial development.

Table 3. Some of the most stable perovskite devices with their configurations, PCEs, stability times and test conditions. In gray, the highest stability for mCPSCs. Adopted from reference [156].

Ref.	Architecture	Test Conditions	PCE (%)	PCE Loss (%)	Stability Time
[157]	FTO/TiO ₂ -Cl/MAPbI ₃ /Spiro/Au	Unsealed, stored in dark, RH < 30%.	21	4	2000 h
[158]	FTO/doped C ₆₀ /mixed perovskite/spiro/Au	Exposed to air, RH ~55%	17.6	20	650 h
[159]	ITO/PEDOT:PSS/MAPbI ₃ /PCBM/EFnPs-F/Al	Exposed to air, RH ~50%	14.3	~10	30 days
[160]	FTO/LBSO/MAPbI ₃ /PTAA/Au	In N ₂ , at 25 °C, illuminated with UV component	21.2	<10	120 h
[27]	FTO/TiO ₂ /TiO ₂ /perovskite (3% AVAI)/ZrO ₂ /Carbon	sealed under an ambient environment, 55 °C, constant illumination without UV component	11.9	none	10,000 h
[28]	FTO/TiO ₂ /TiO ₂ /AVA-MAPbI ₃ /ZrO ₂ /Carbon	maximum power point of 55 °C ± 5 °C, following the IEC61215:2016 Standards		none	9000 h
[160]	ITO/TiO ₂ /TiO ₂ /perovskite/PTAA/Au	In N ₂ , high temp. and illumination	20.6	10	160 h
[161]	FTO/TiO ₂ /TiO ₂ /mixed perovskite/Spiro-OMeTAD-SWCNT	N ₂ atmosphere, LED white light equivalent to 1 Sun, and at 60 °C	15	20	580 h

5. Conclusions

The fully printable triple mesoscopic carbon perovskite solar cell (mCPSC) attracts interest due to its unique stability and high potential for cheap commercialisation in the PV market. This report provides a comprehensive and critical overview of recent advancements in the processing and materials used in both the inorganic stack and perovskite solution and highlights potential opportunities for further research. As one of the key aspects of future commercialisation, the outstanding stability of the mCPSC is also covered in detail.

It is clear that the working mechanisms of these thick devices require further clarification. Strongly related to both the chemical composition of the absorber and the degree of stack infiltration, the mechanisms behind perovskite-mediated efficiency and lifetime enhancements also merit further investigation, calling for continued research into the infiltration of novel perovskite formulations.

Perhaps the most promising avenue for efficiency enhancement lies in improving the electrical properties and overall performance of the carbon top electrode. Currently, many ETL and HTL materials remain untested in these devices.

From an engineering perspective a great deal of work is required, from areas such as process automation and successful deposition of the layers on flexible substrates are key to unlocking large volume production.

Having passed qualification tests with 9000 h operational tracking, mCPSCs represent a huge step forward for device stability compared to other PSCs. Further research is required to achieve performance-stable modules with commercially competitive decade-long lifetimes, but mCPSCs currently represent the most promising PSC for reaching

this goal. Achieving this target will likely require contributions from many fields, from improving on perovskite formulations to optimising encapsulation methods.

The mCPSC is both a front runner for early PSC commercialisation and a scientific opportunity to learn more about the fundamental properties and versatility of perovskite materials. With so many potential avenues for future research, these architectures are likely to attract increasing interest over the coming years.

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